

FIG. 1

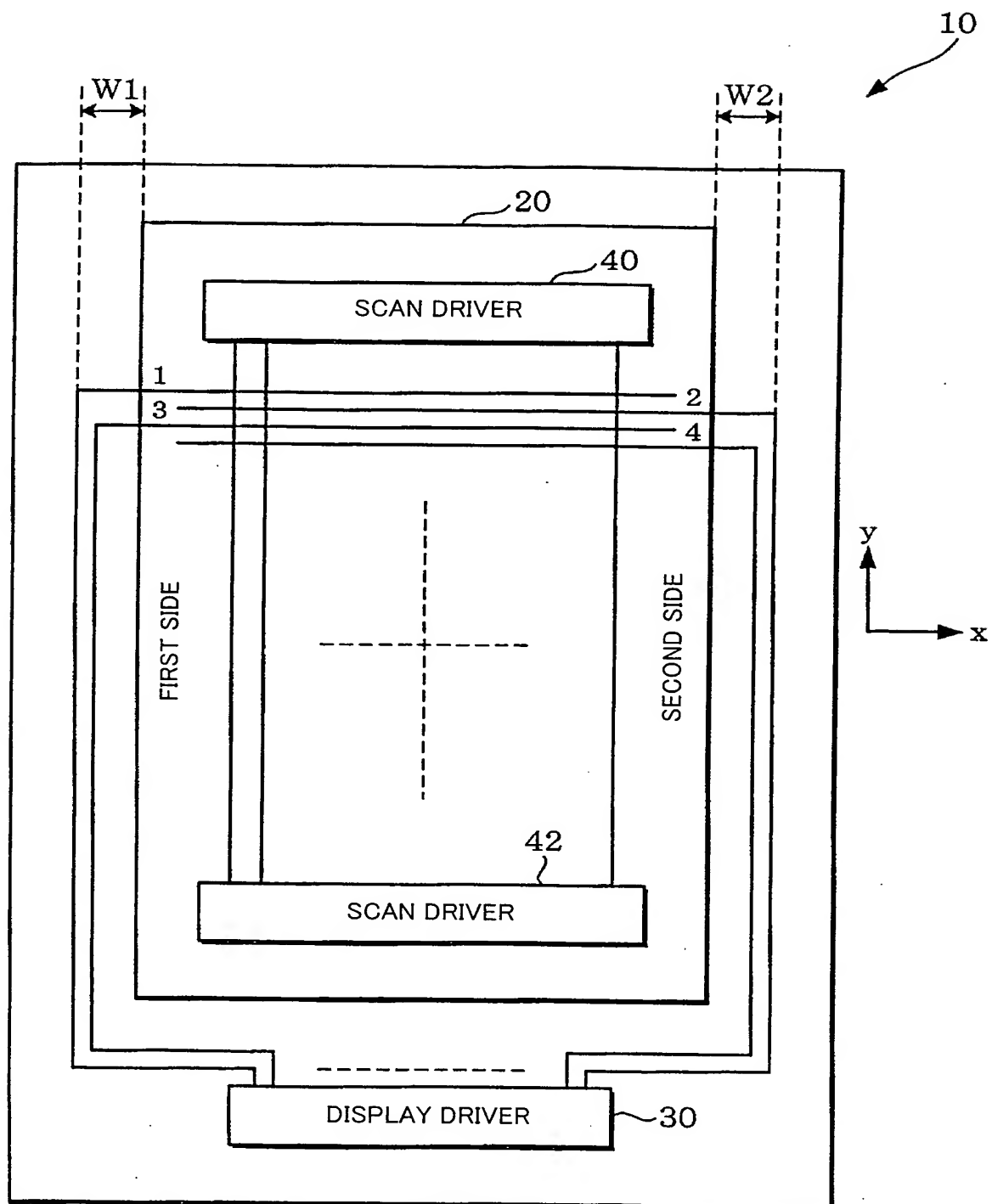


FIG. 2

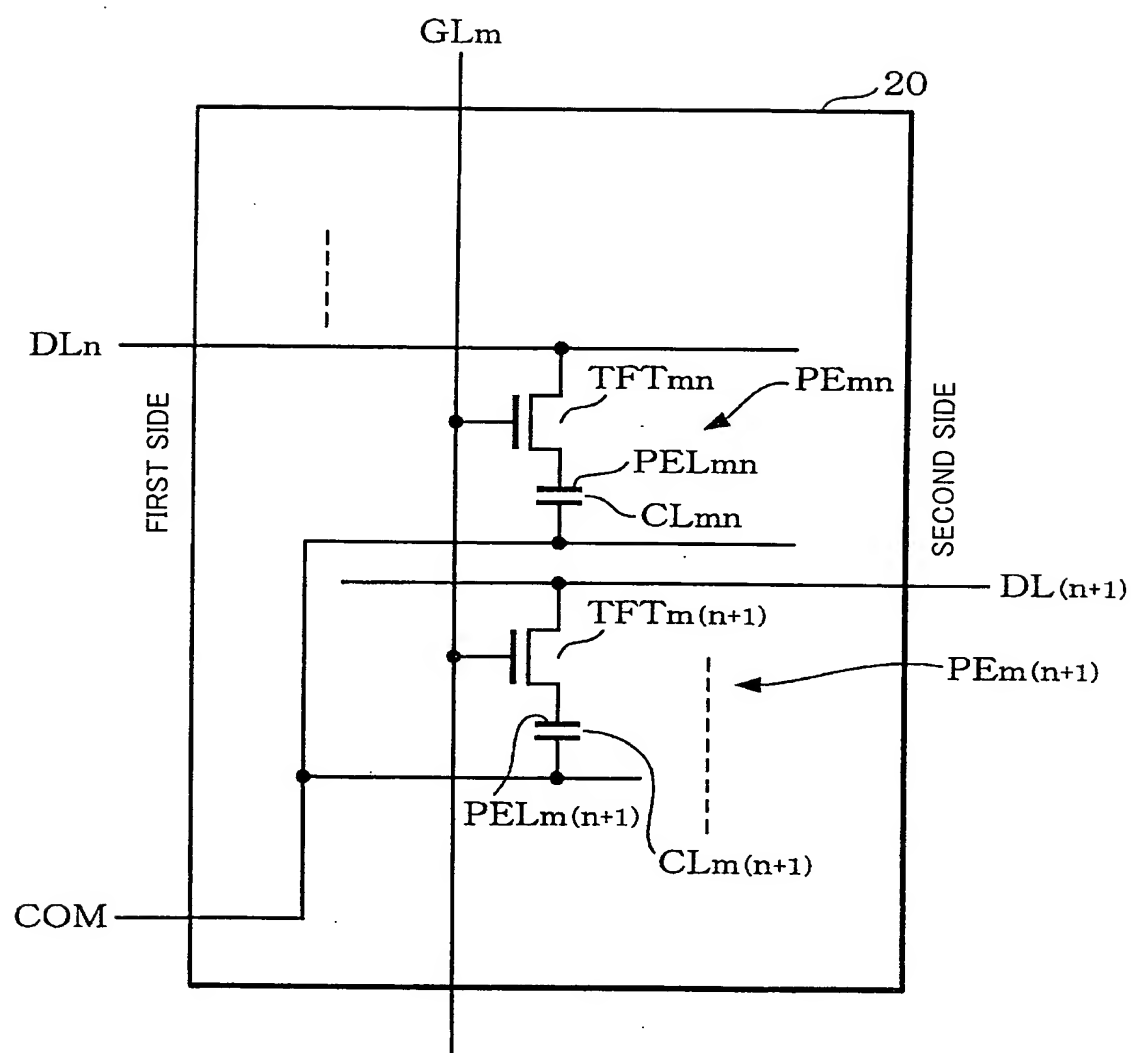


FIG. 3

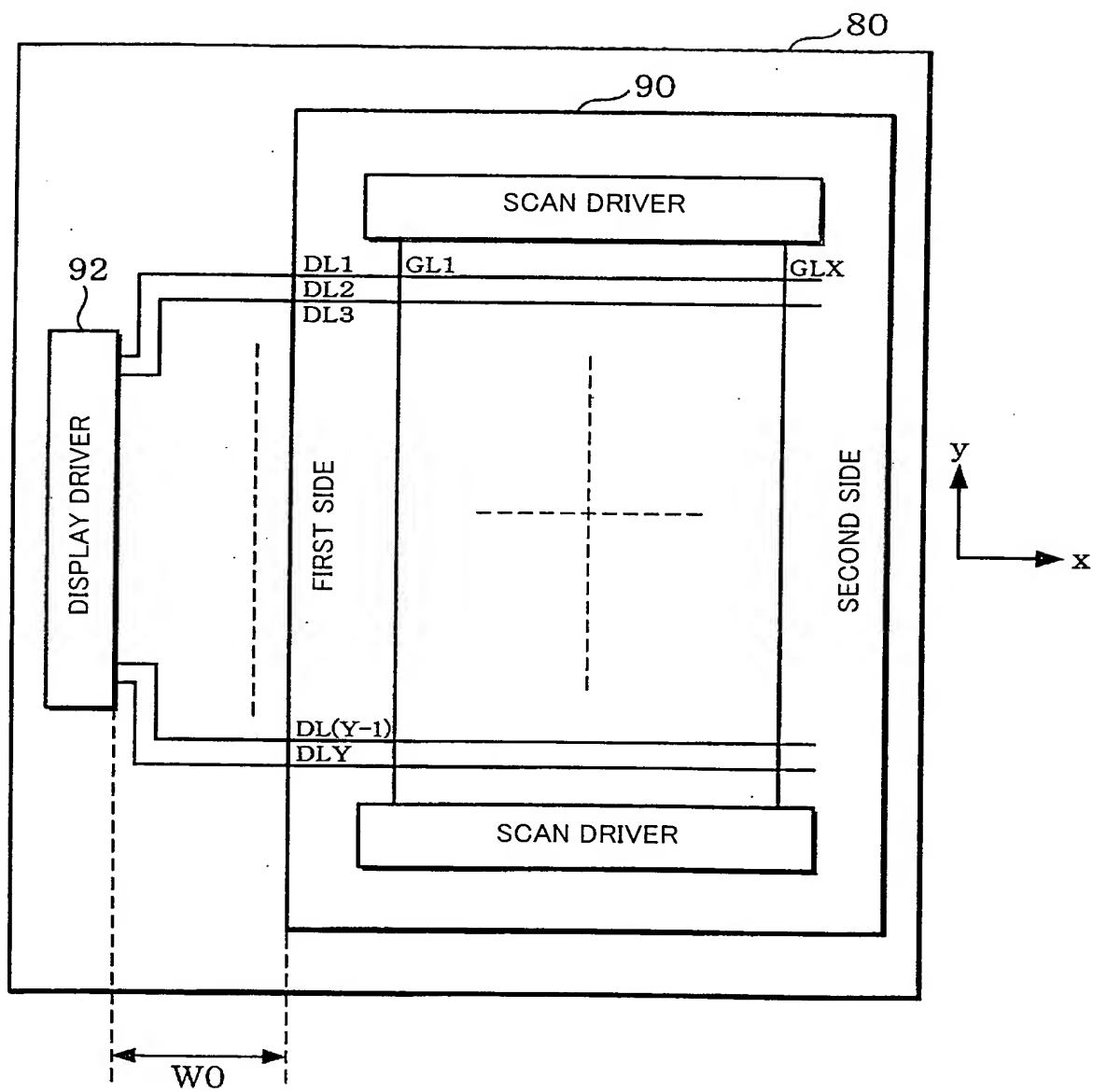


FIG. 4

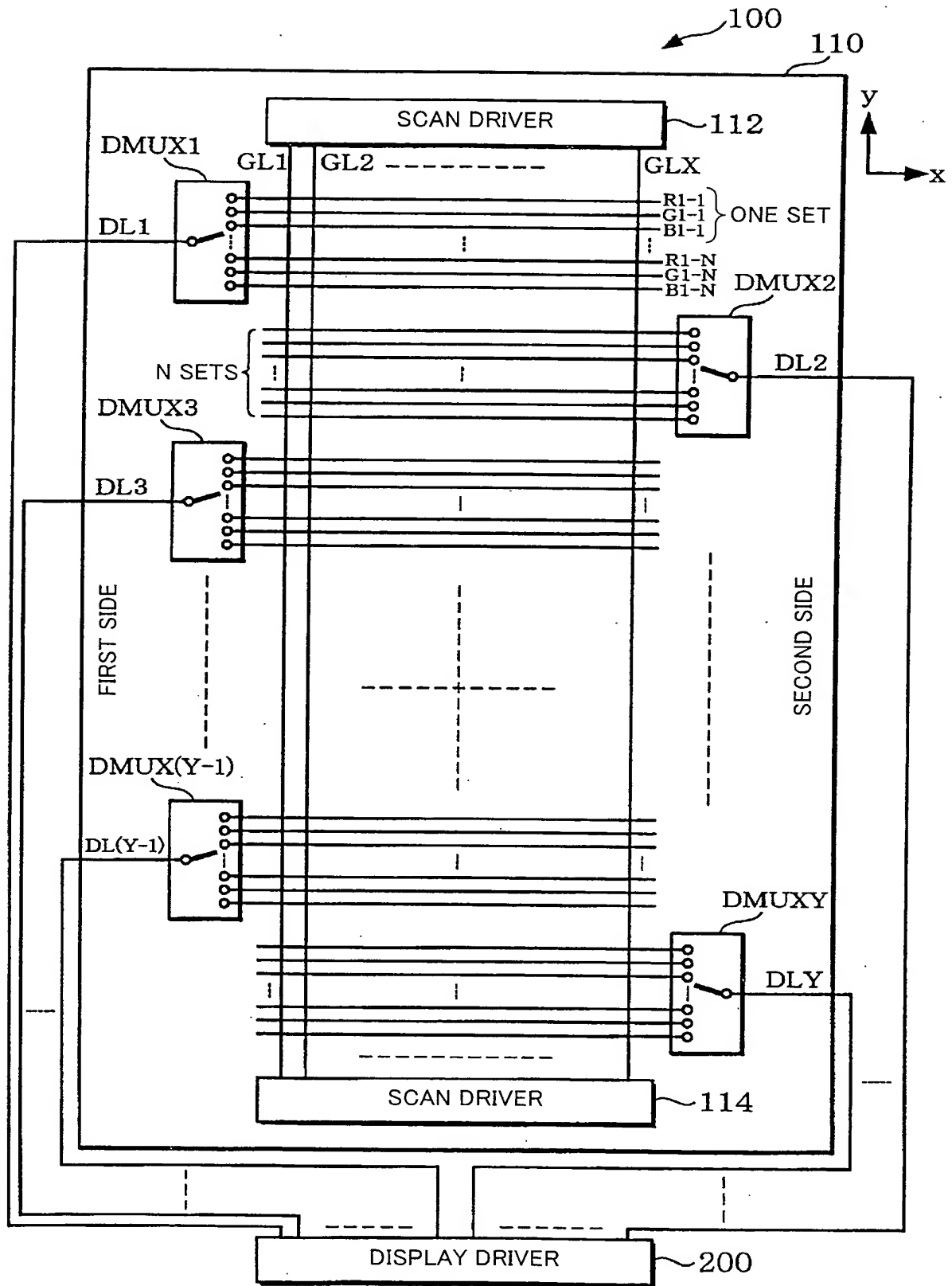


FIG. 5

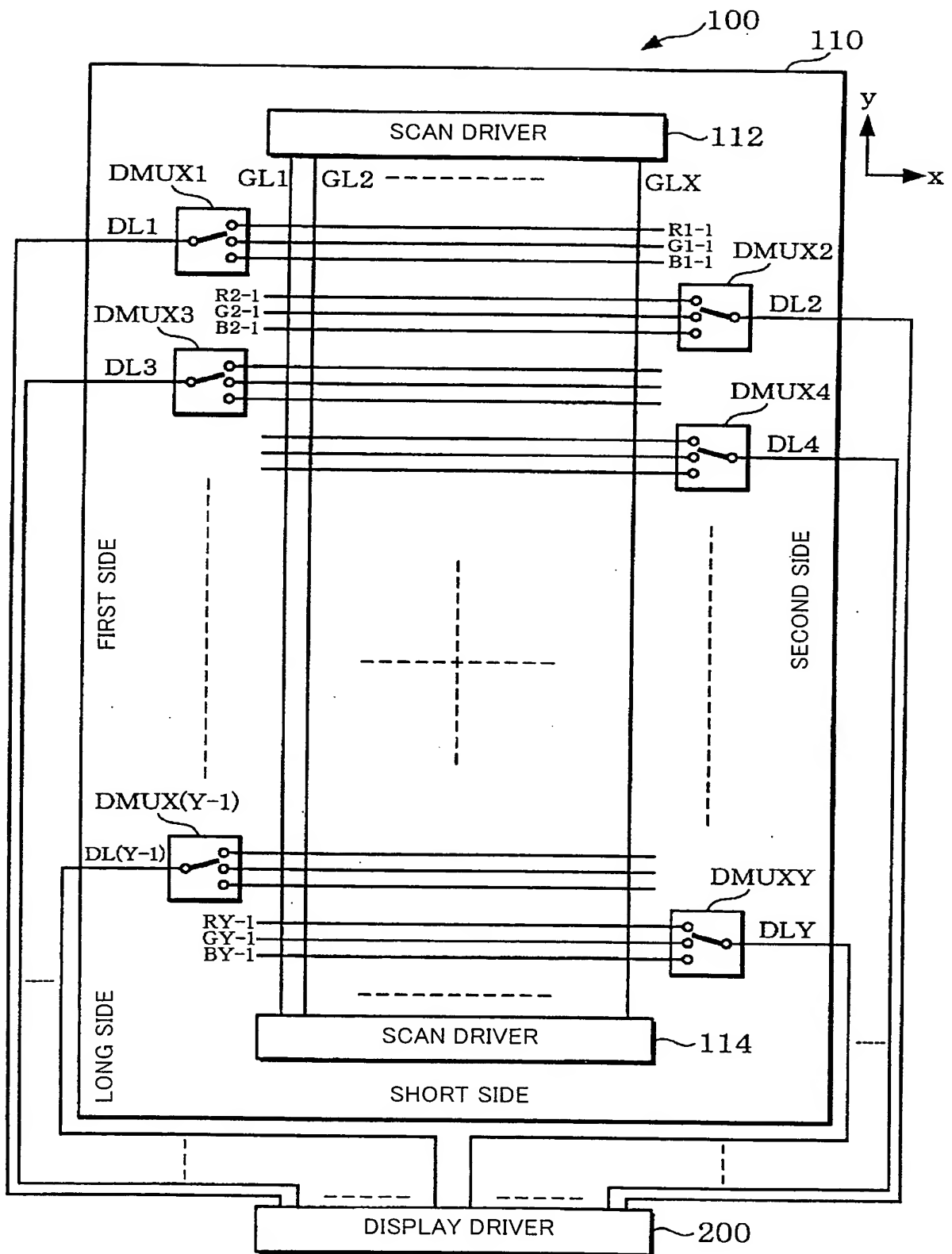


FIG. 6

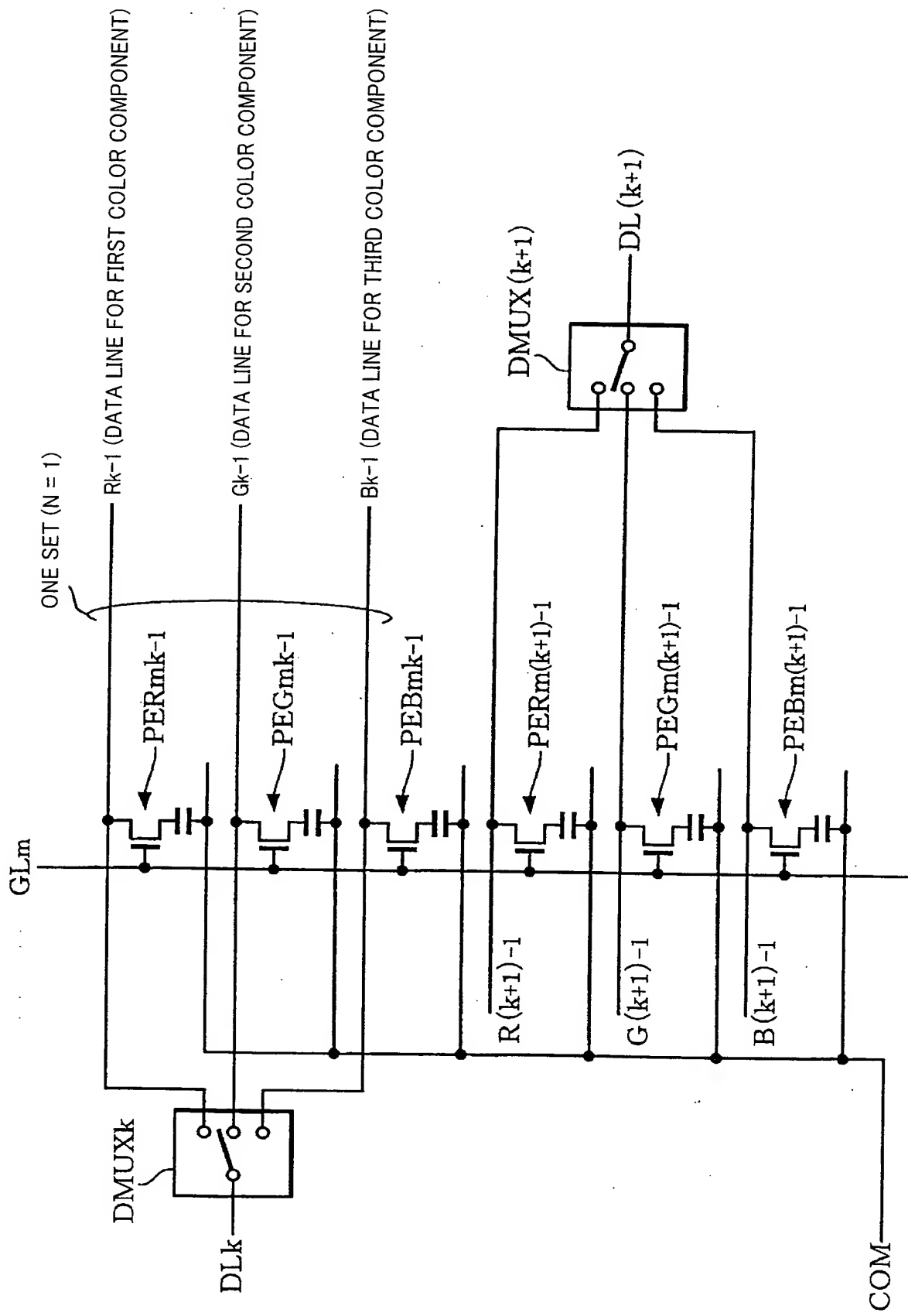


FIG. 7A

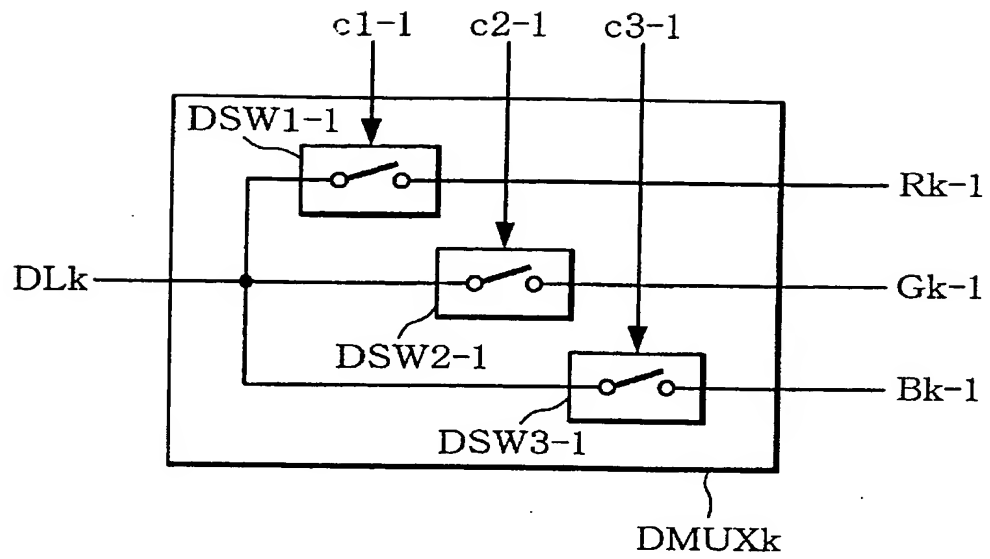


FIG. 7B

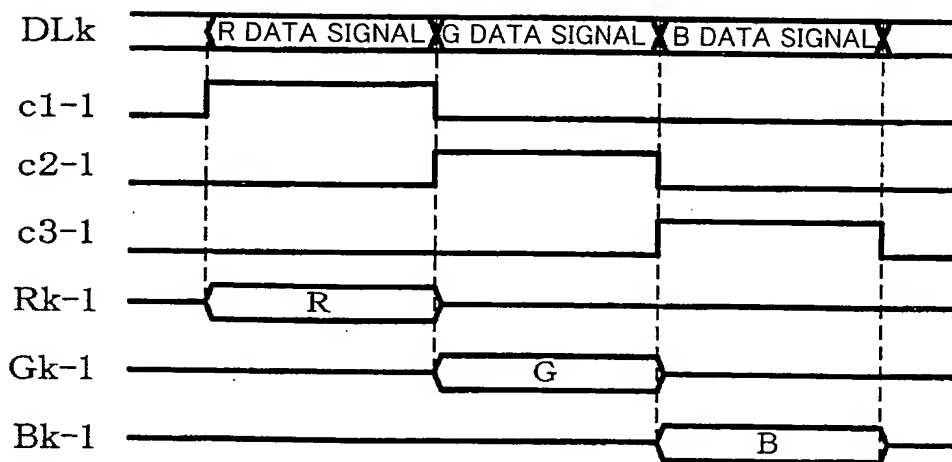


FIG. 8

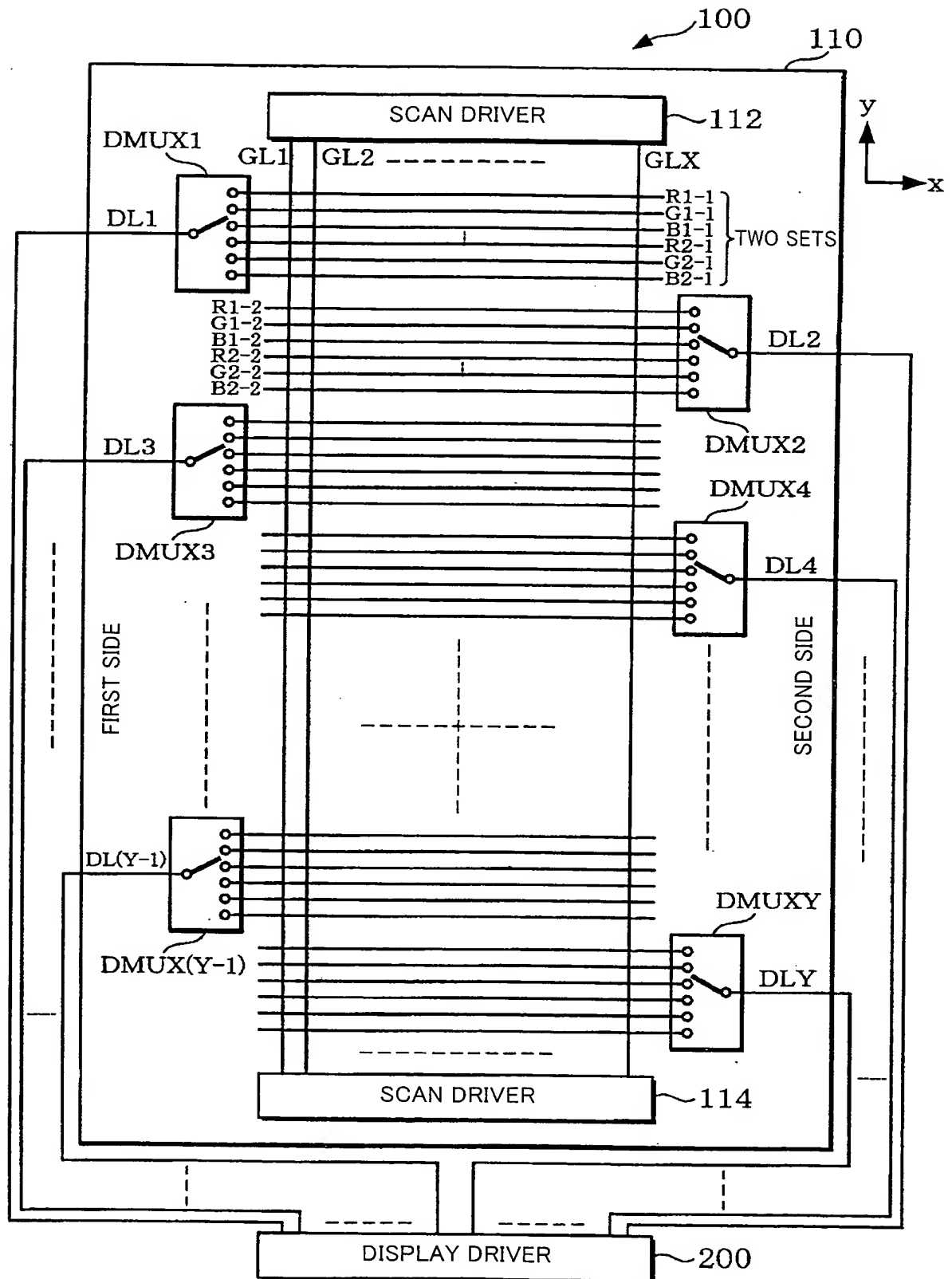


FIG. 9A

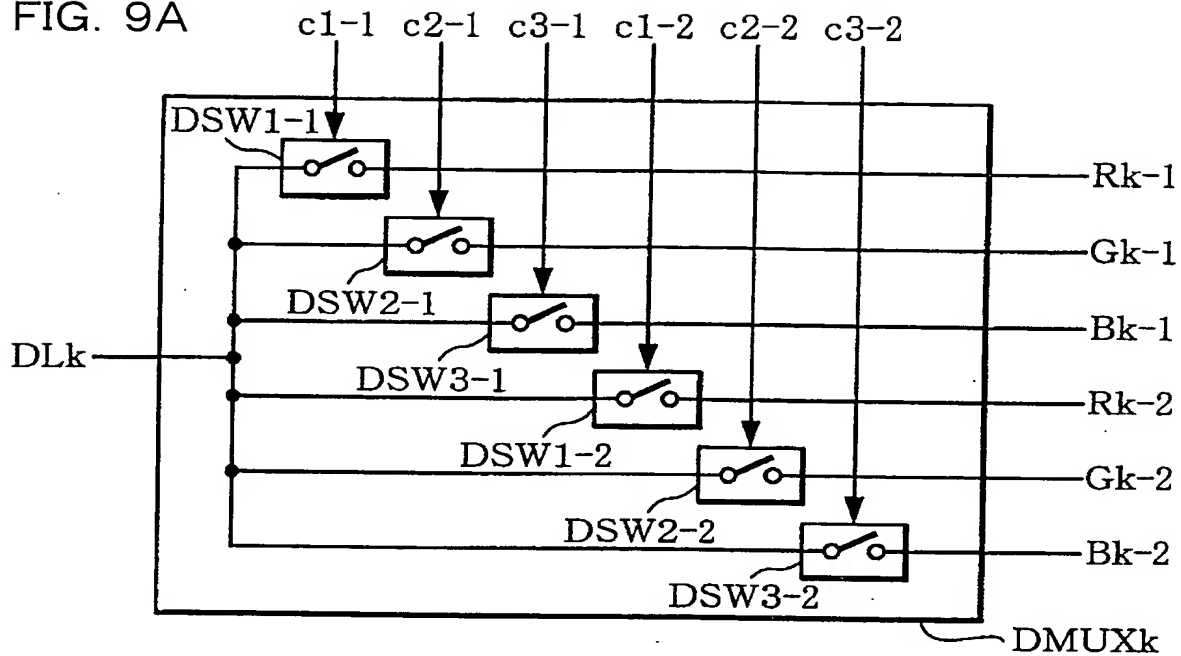


FIG. 9B

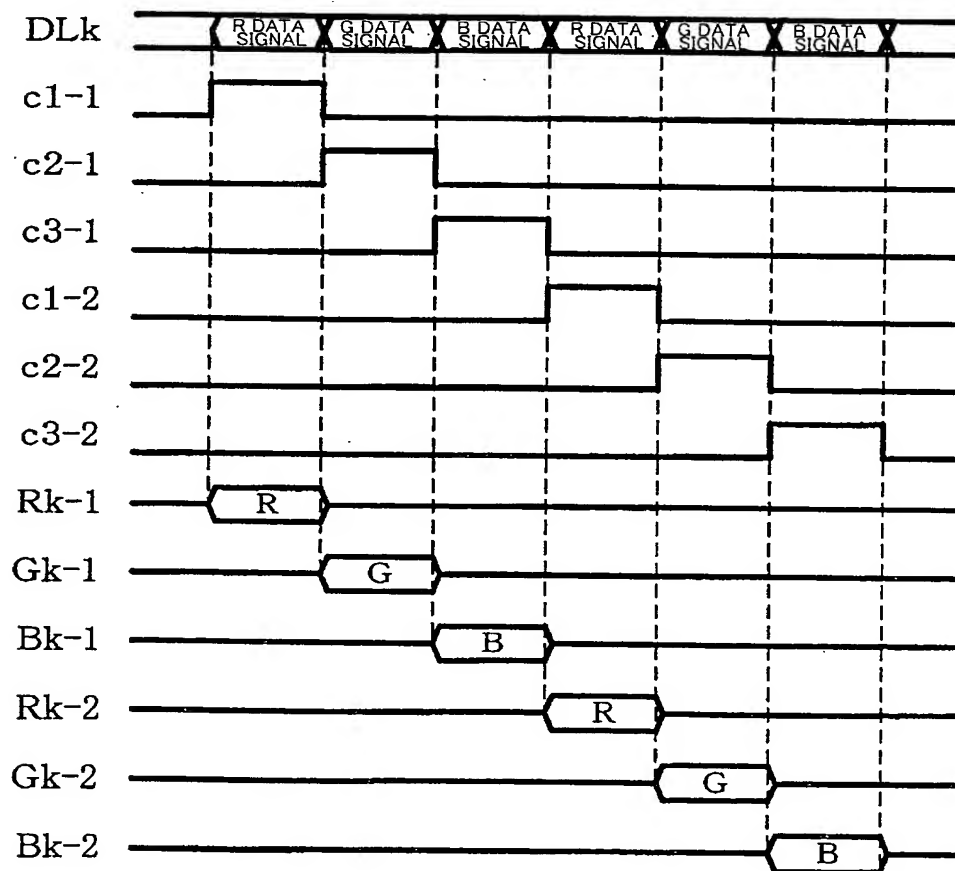


FIG. 10

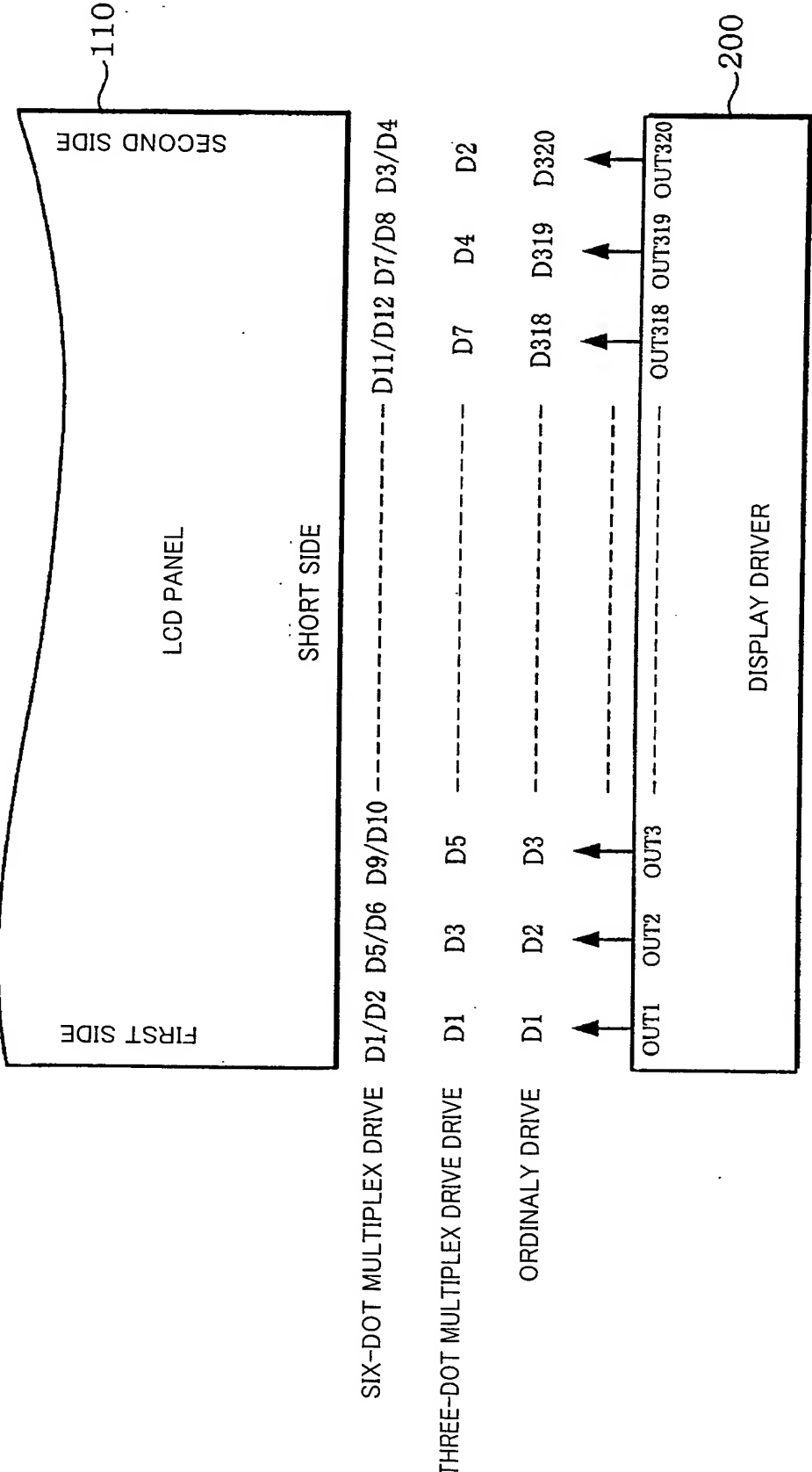


FIG. 11

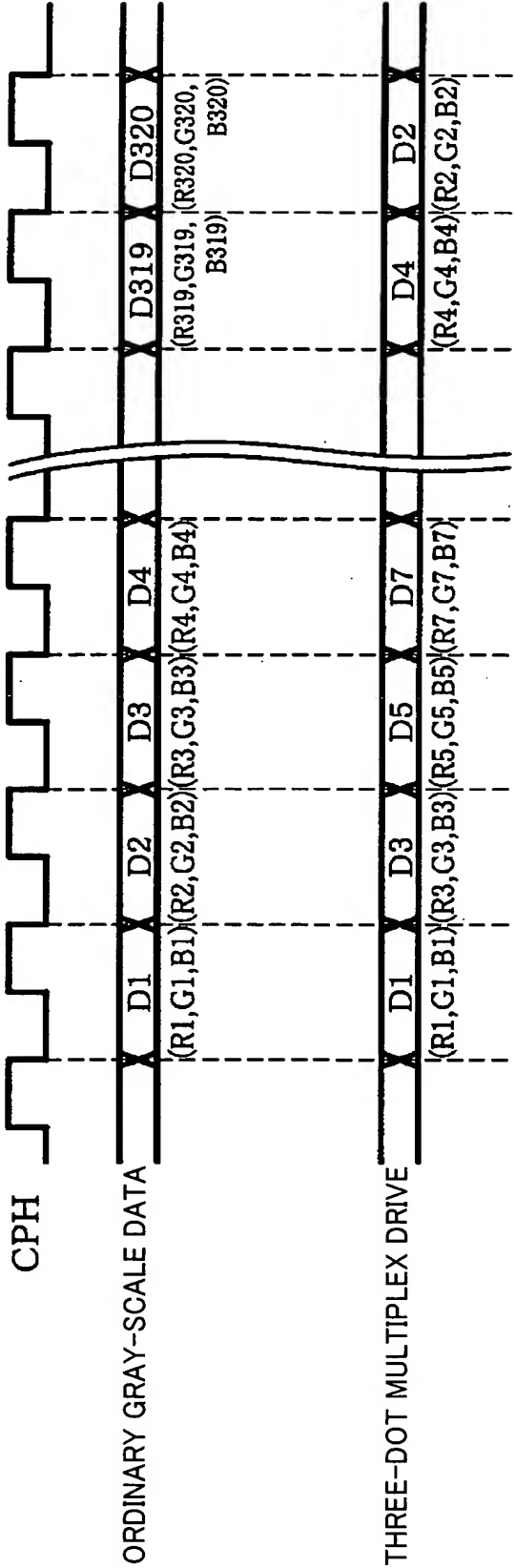


FIG. 12

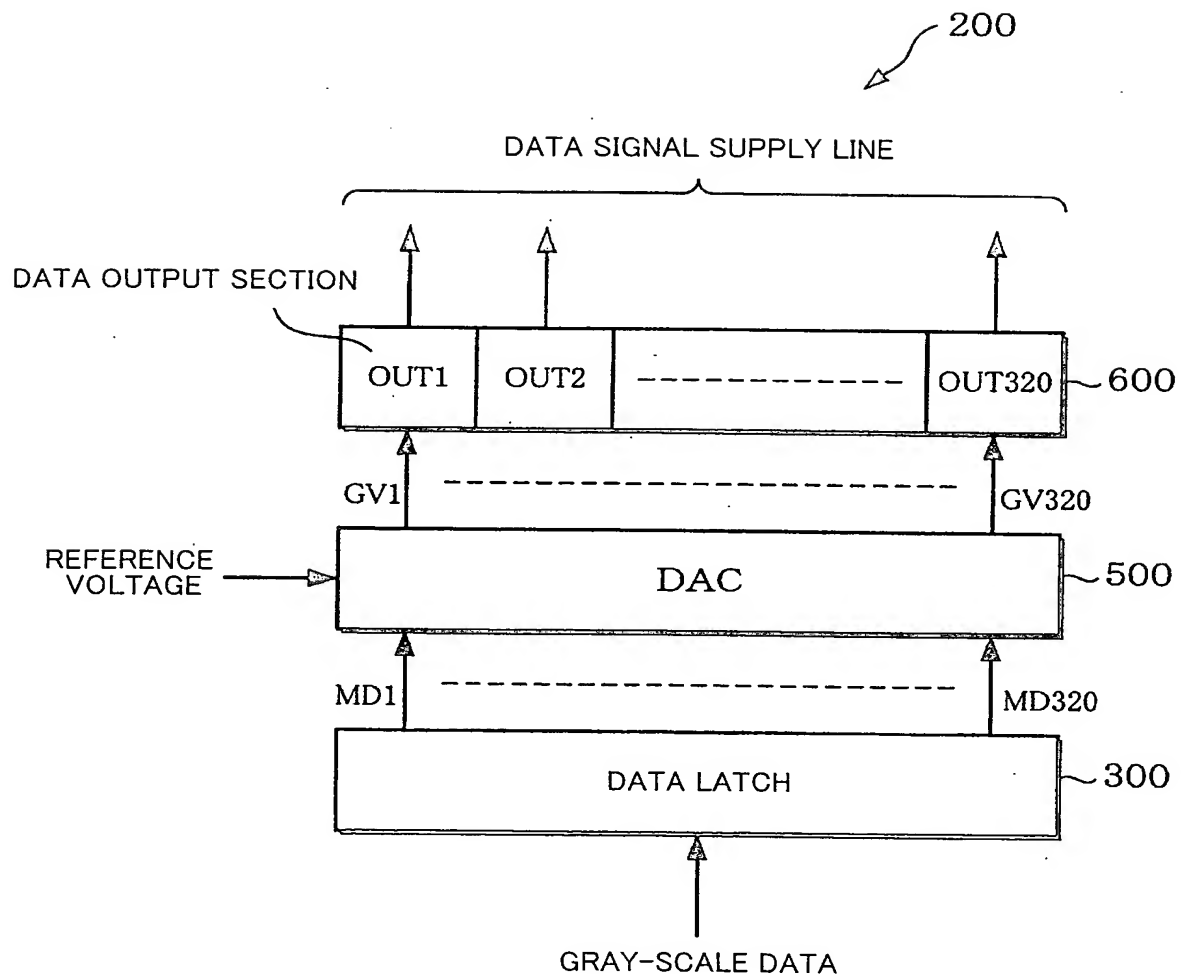


FIG. 13

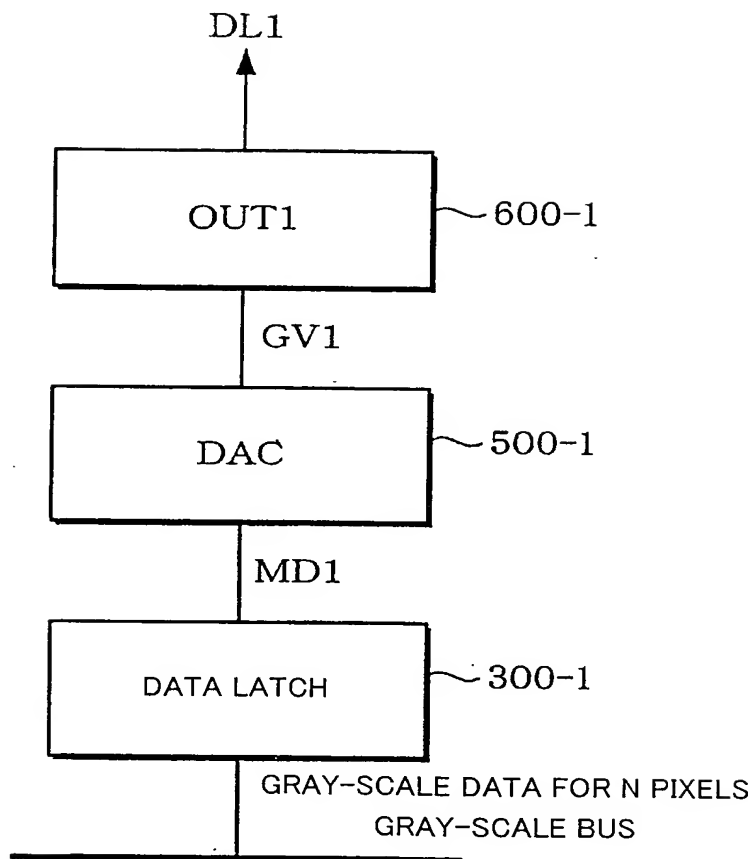


FIG. 14

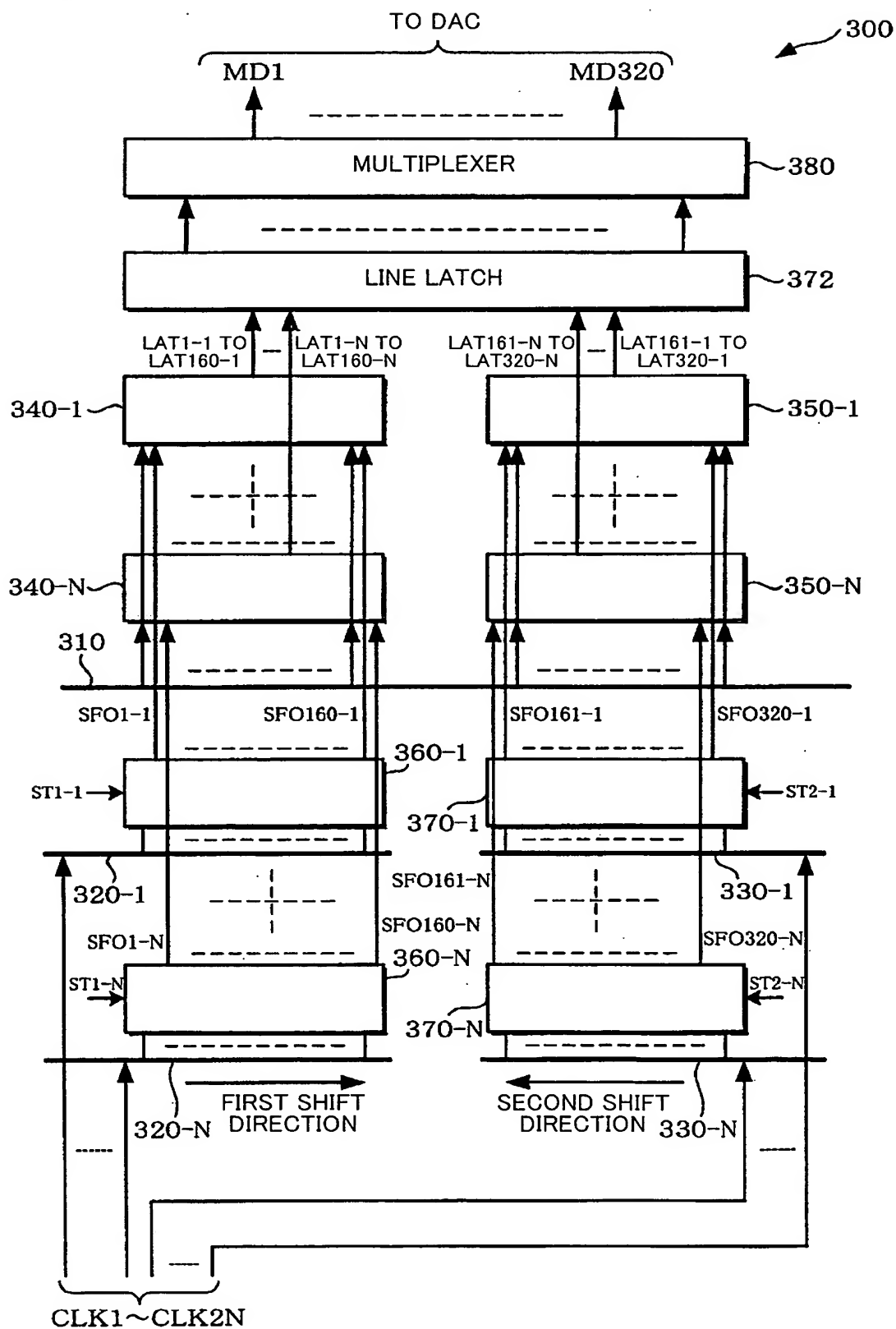


FIG. 15

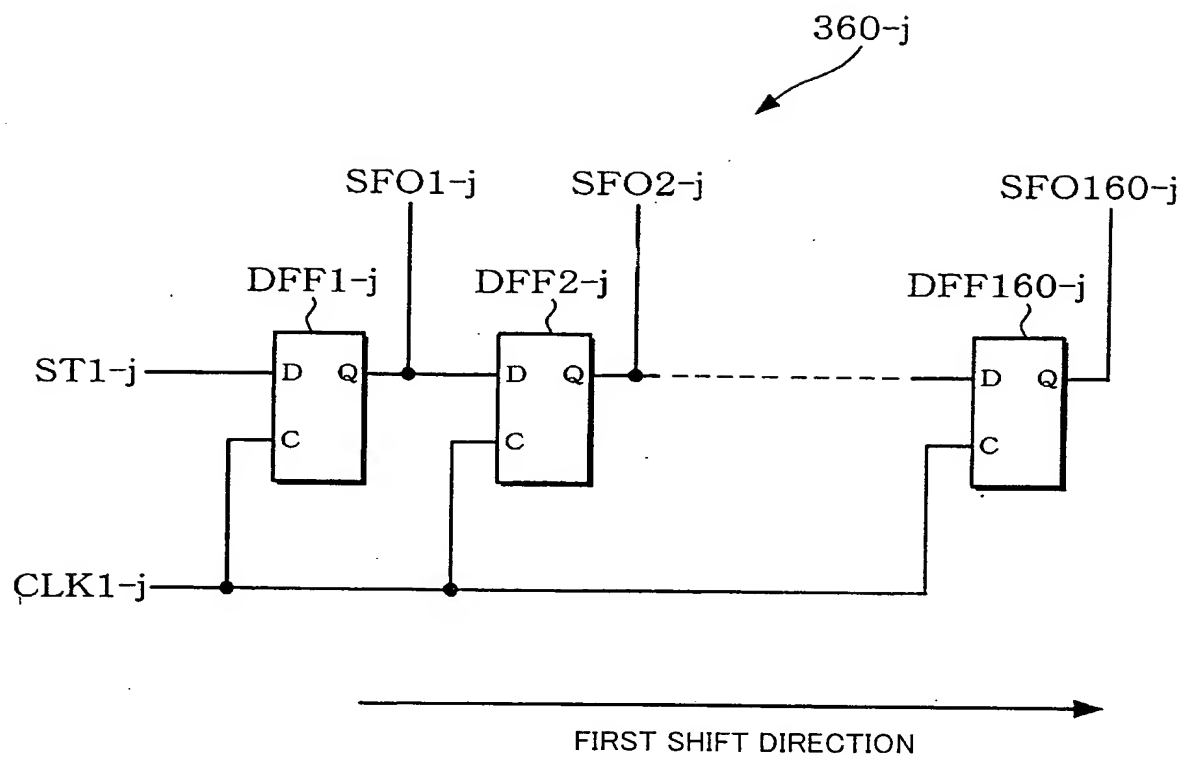


FIG. 16

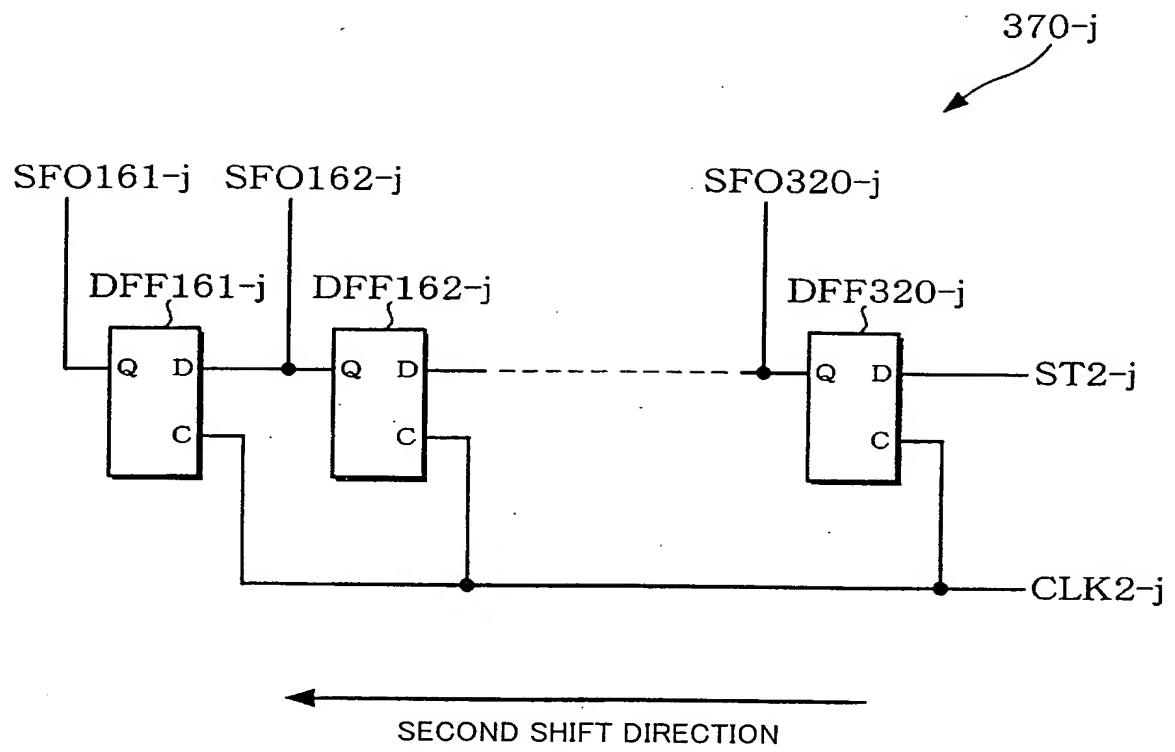


FIG. 17

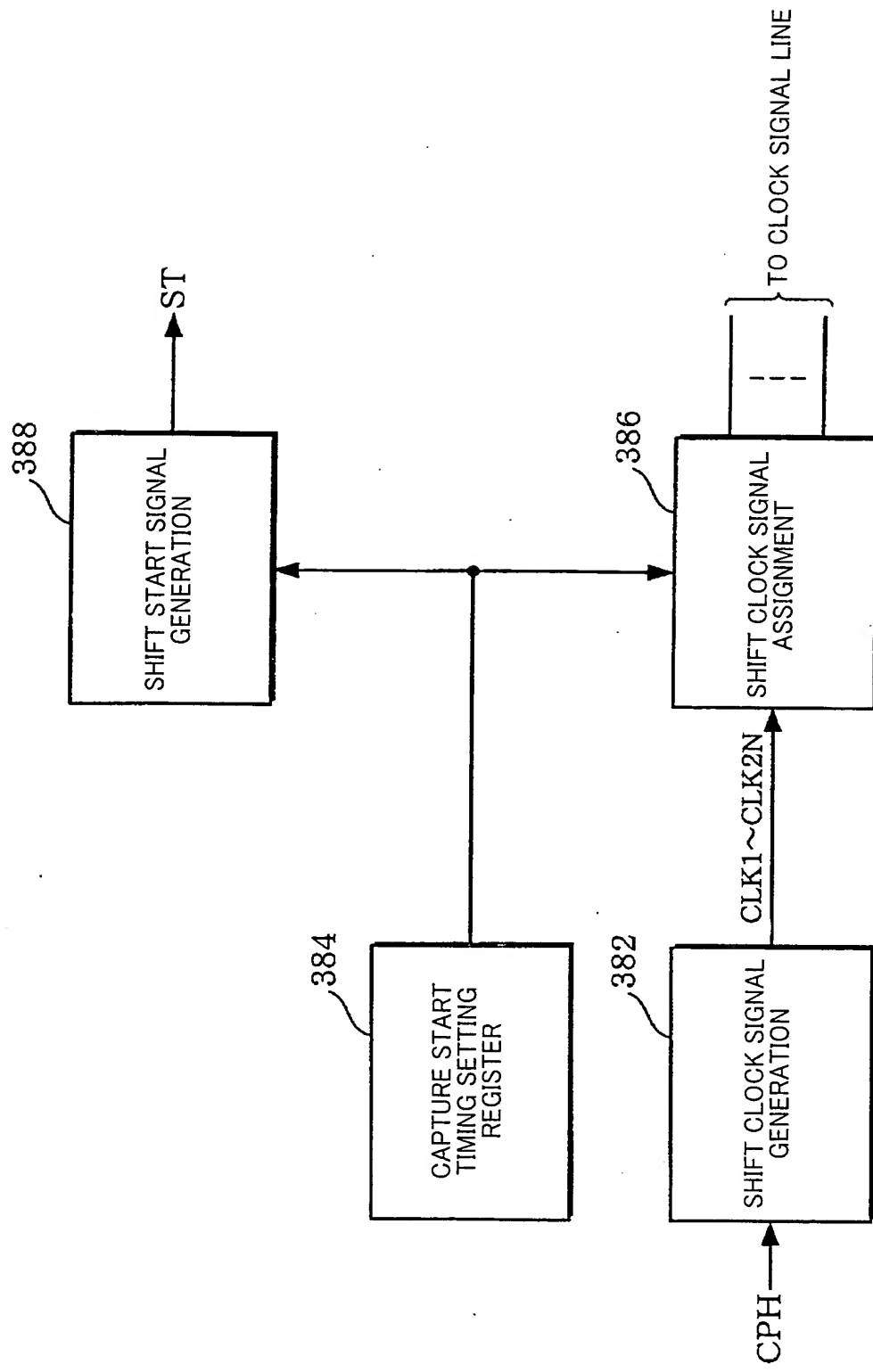


FIG. 18

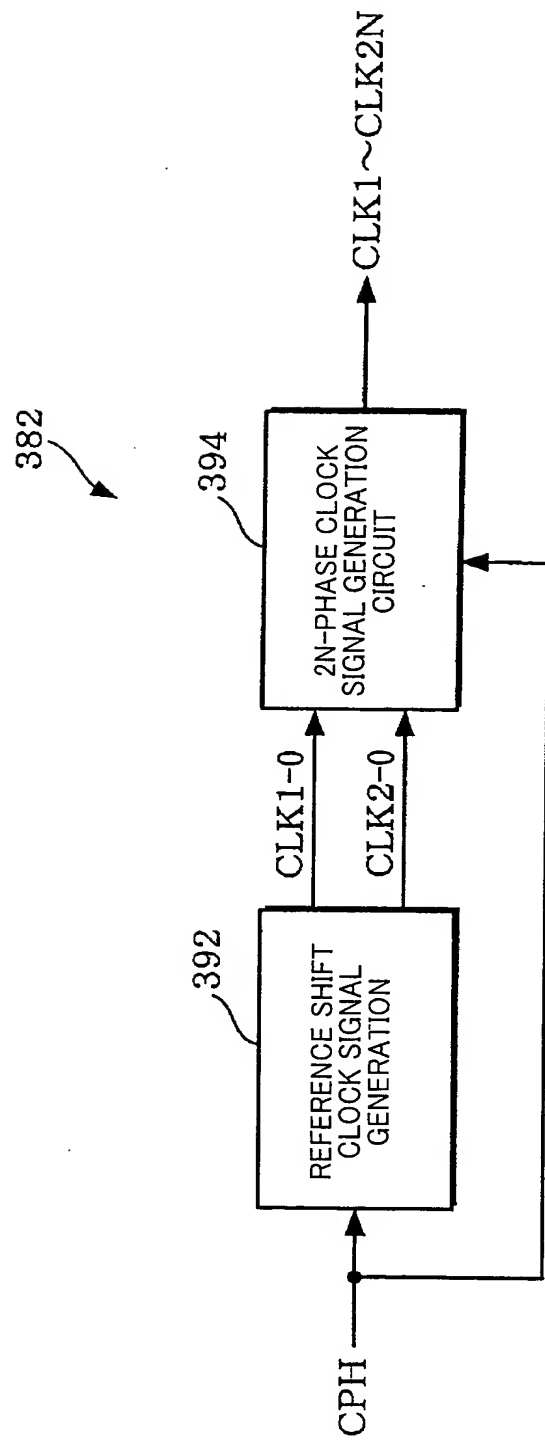


FIG. 19

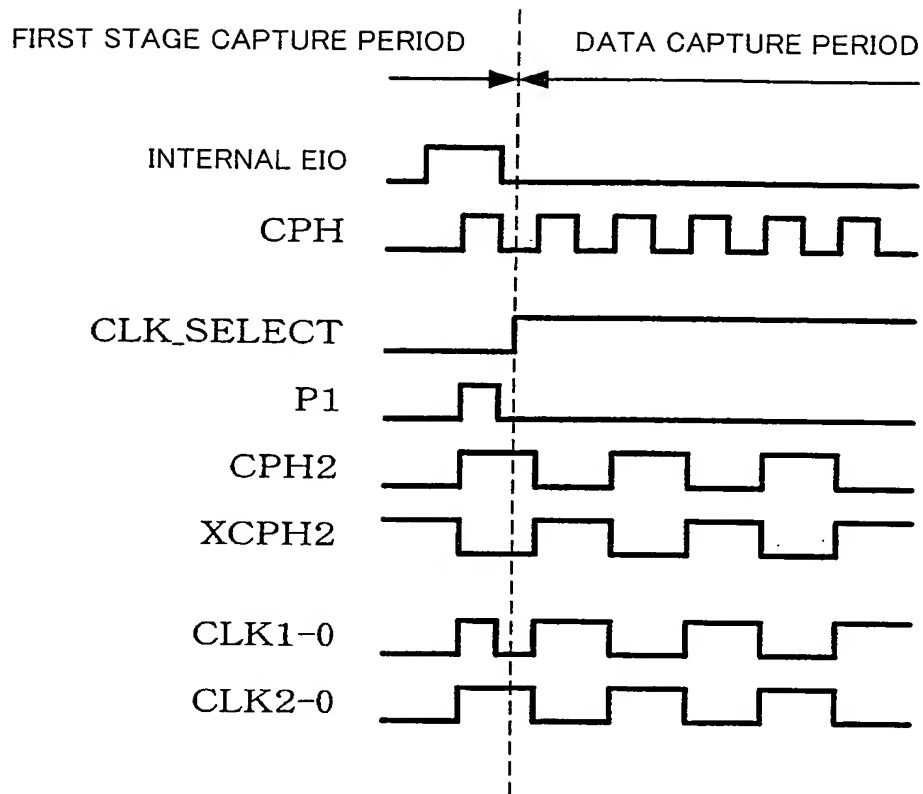


FIG. 20

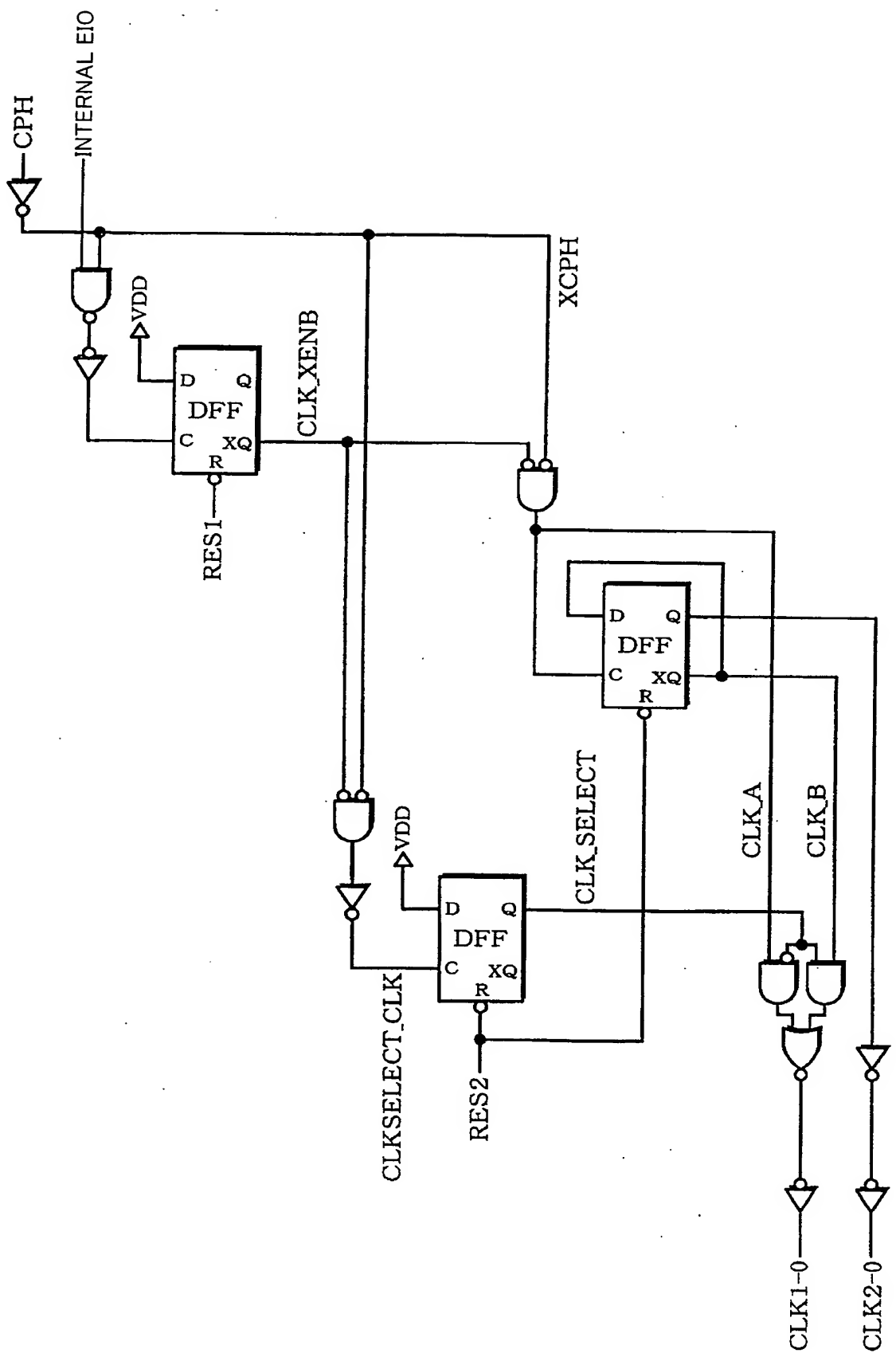


FIG. 21

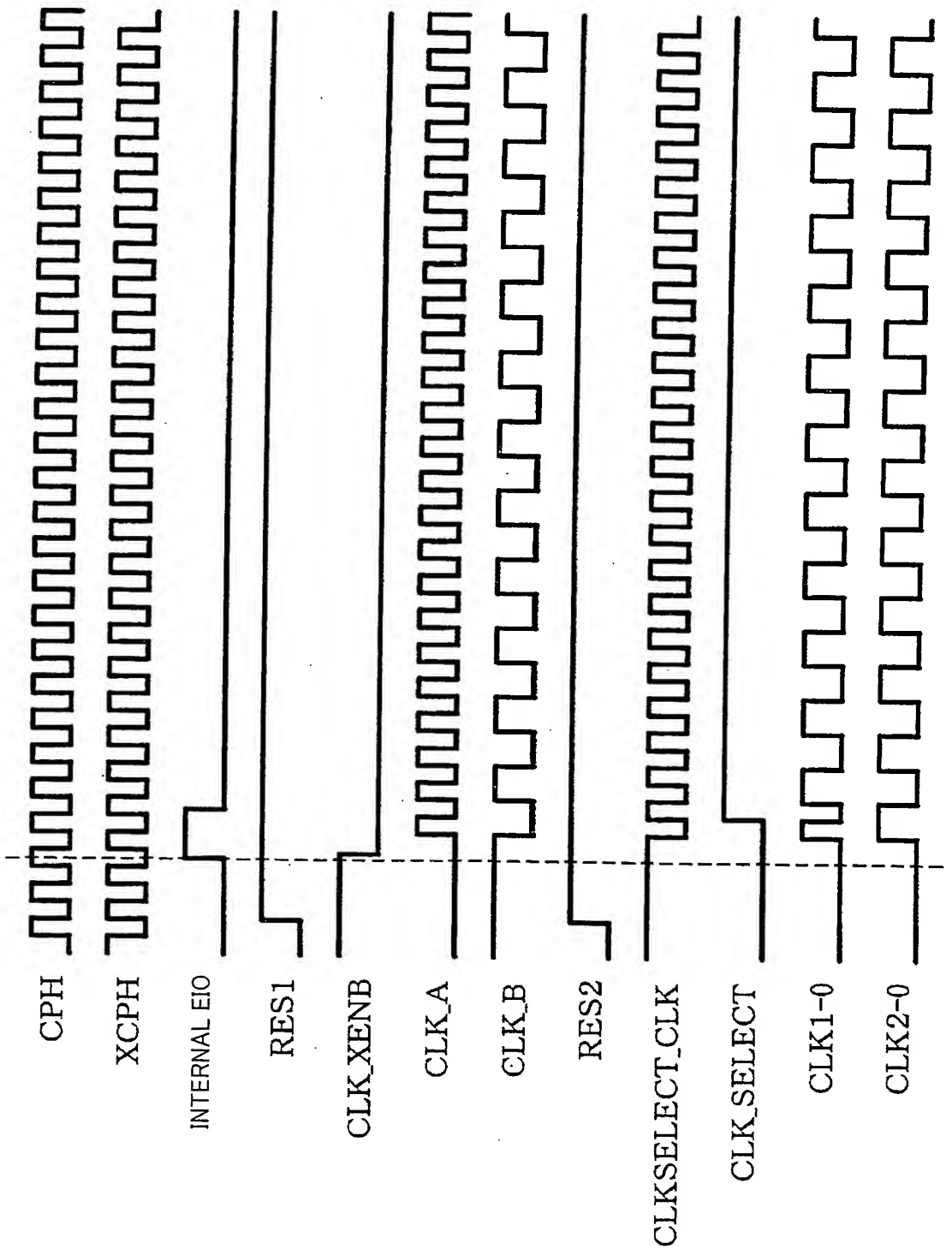


FIG. 22

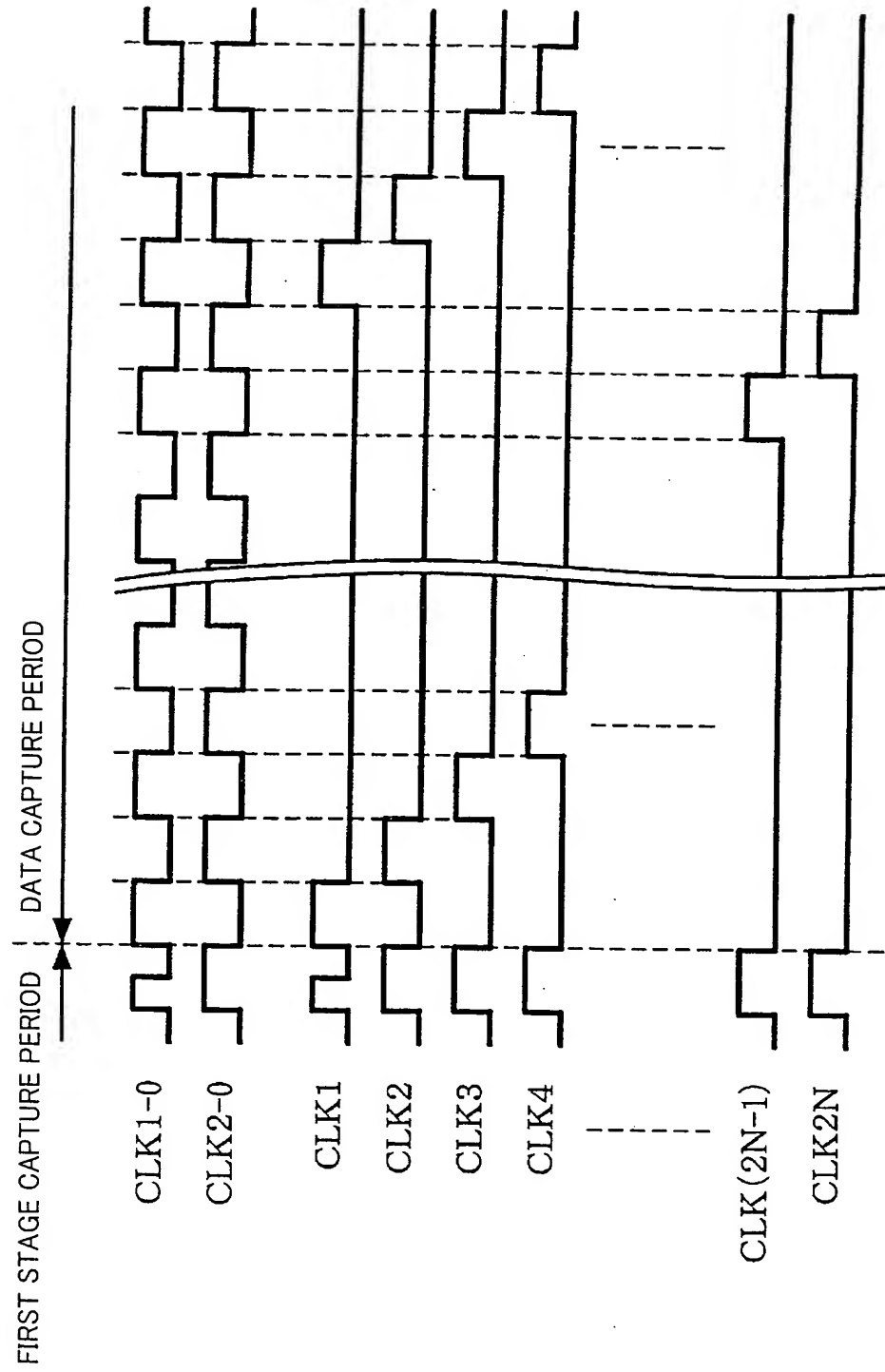


FIG. 23

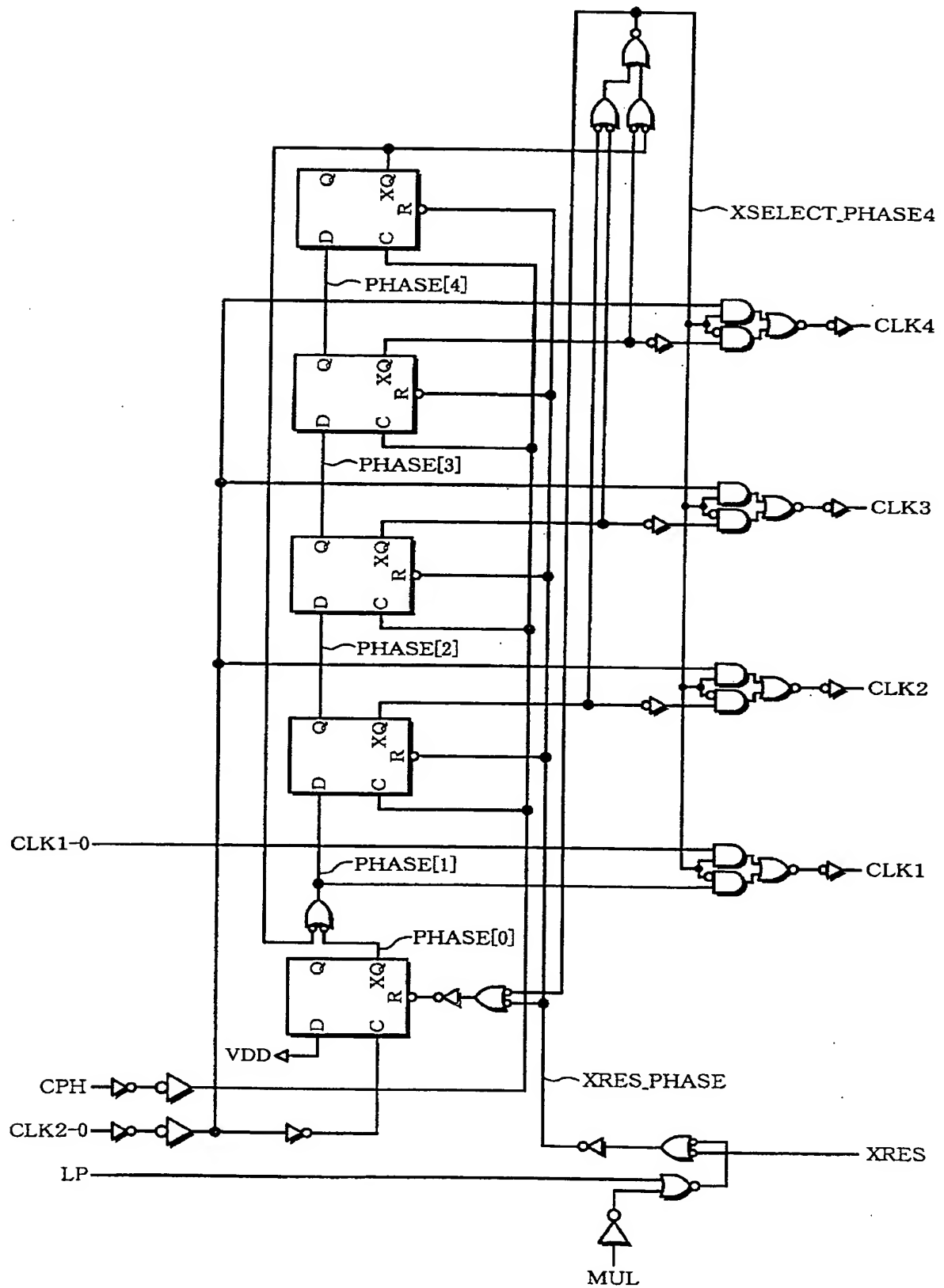


FIG. 24

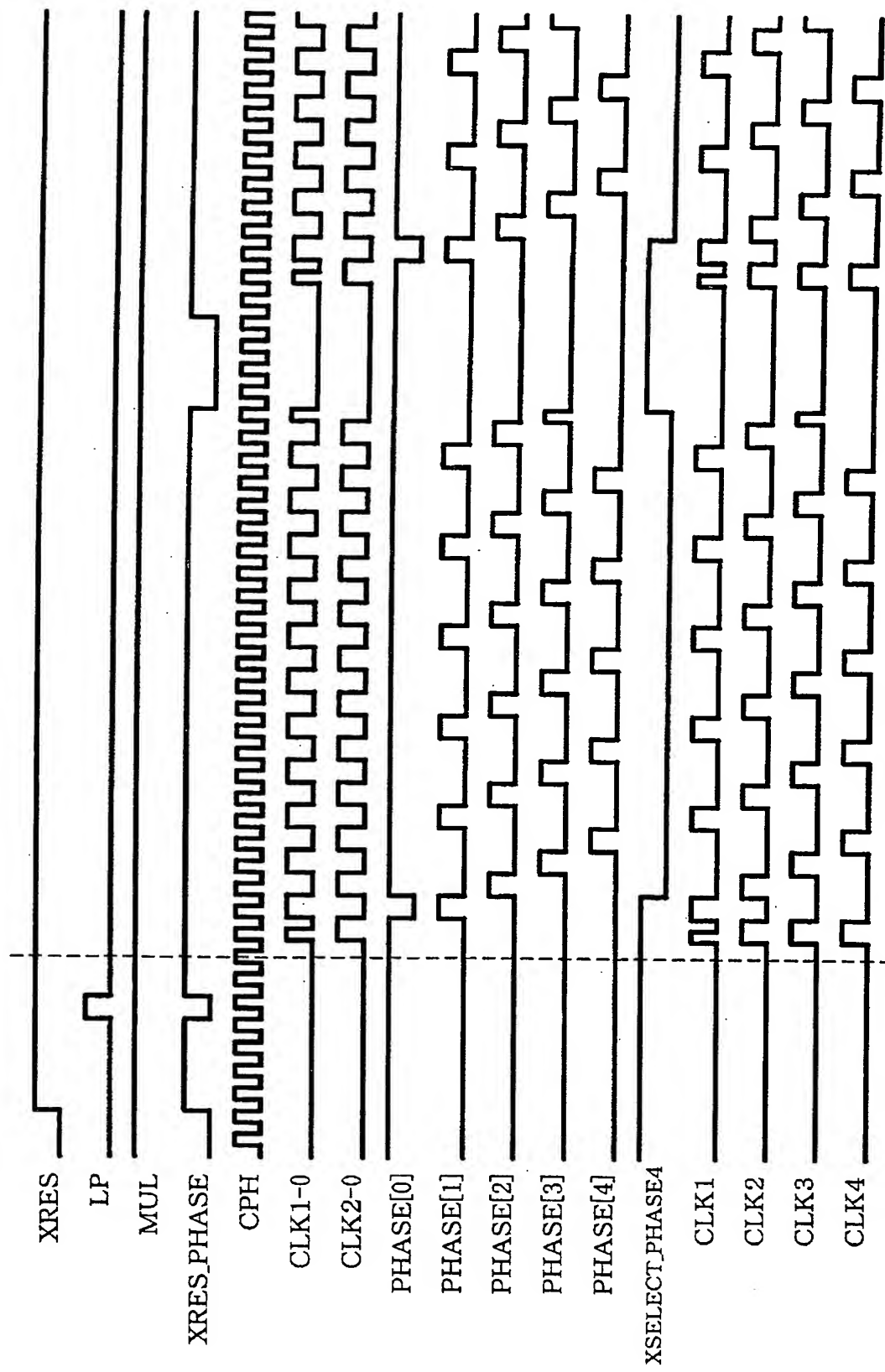


FIG. 25

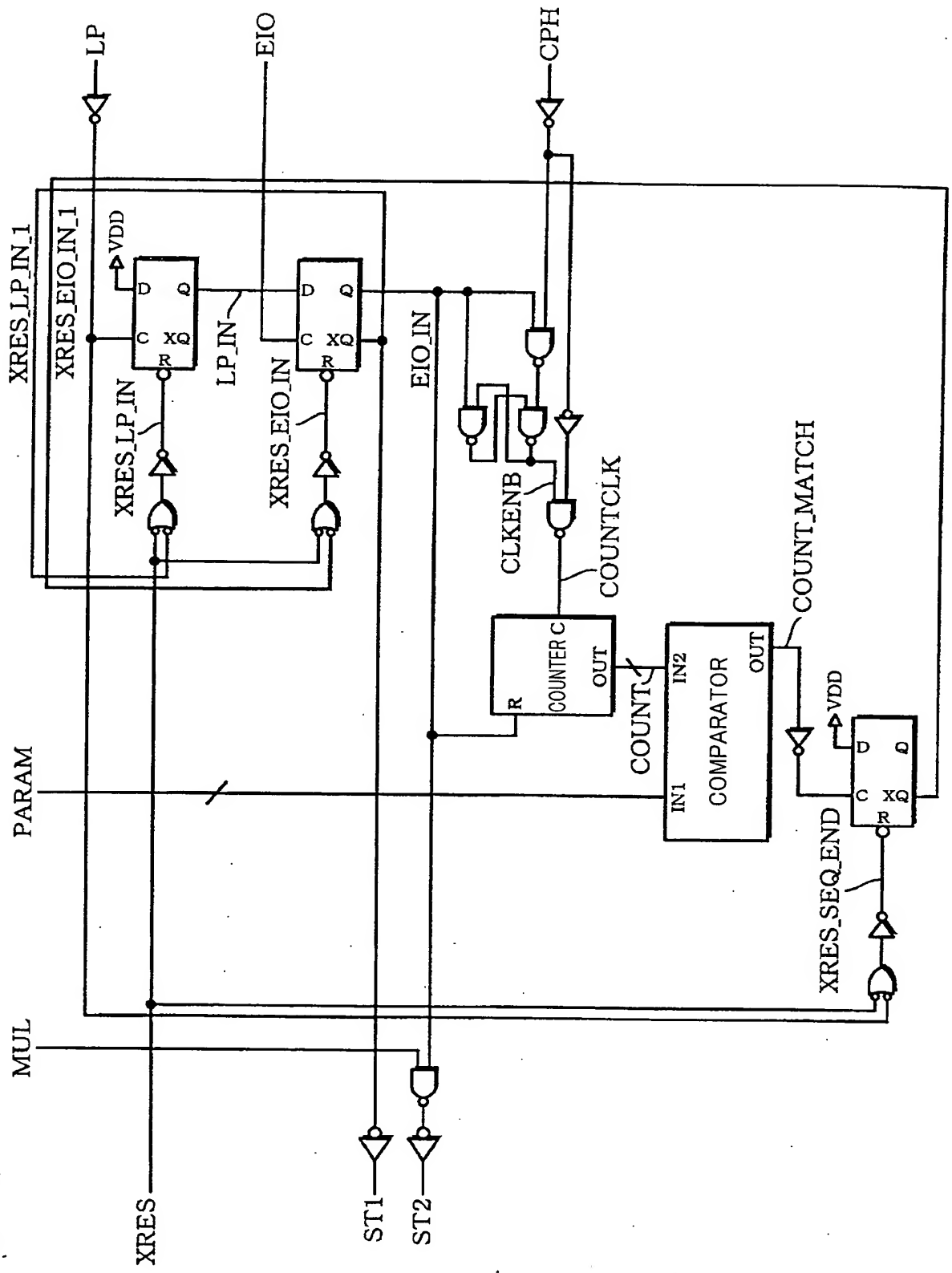


FIG. 26

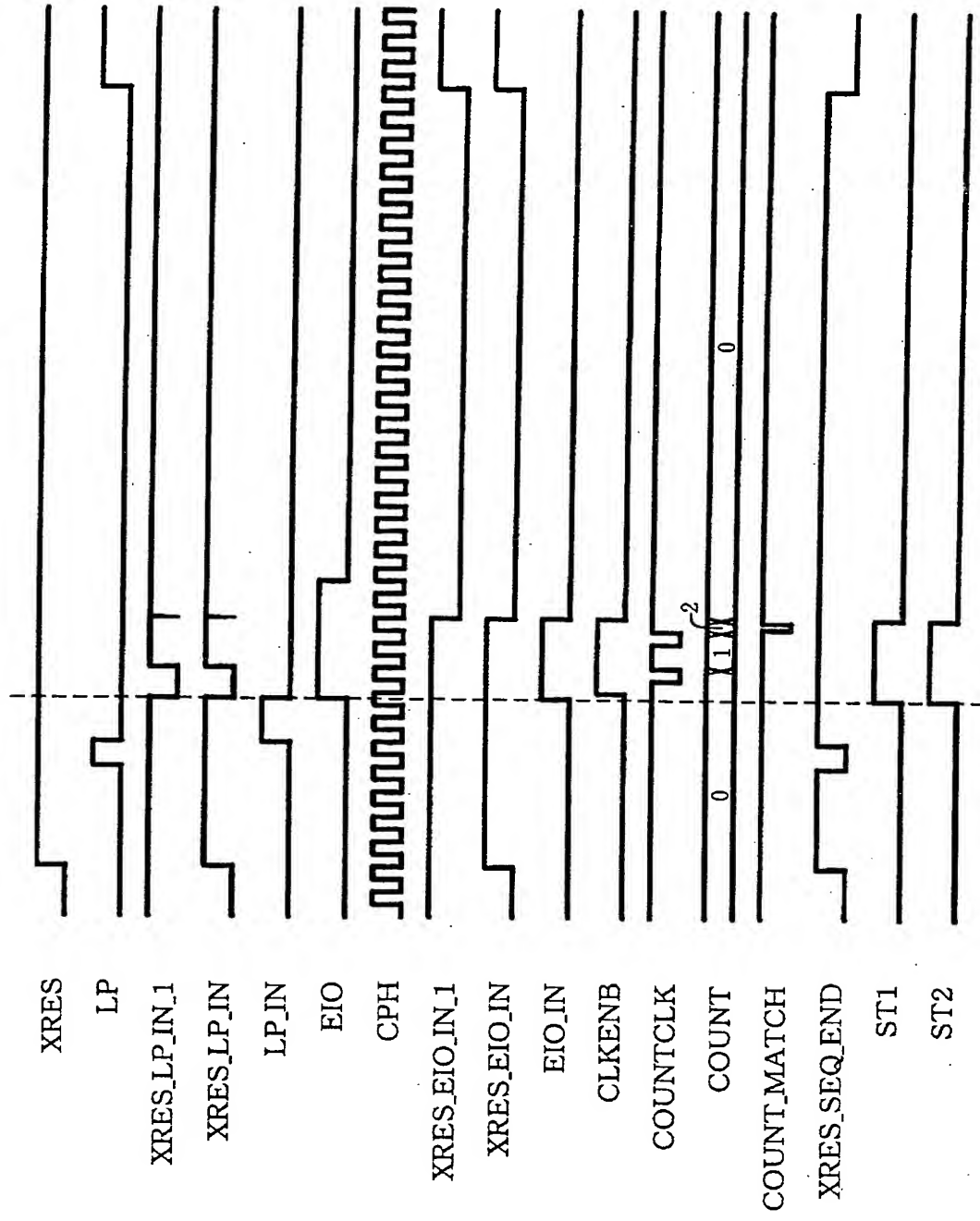


FIG. 27

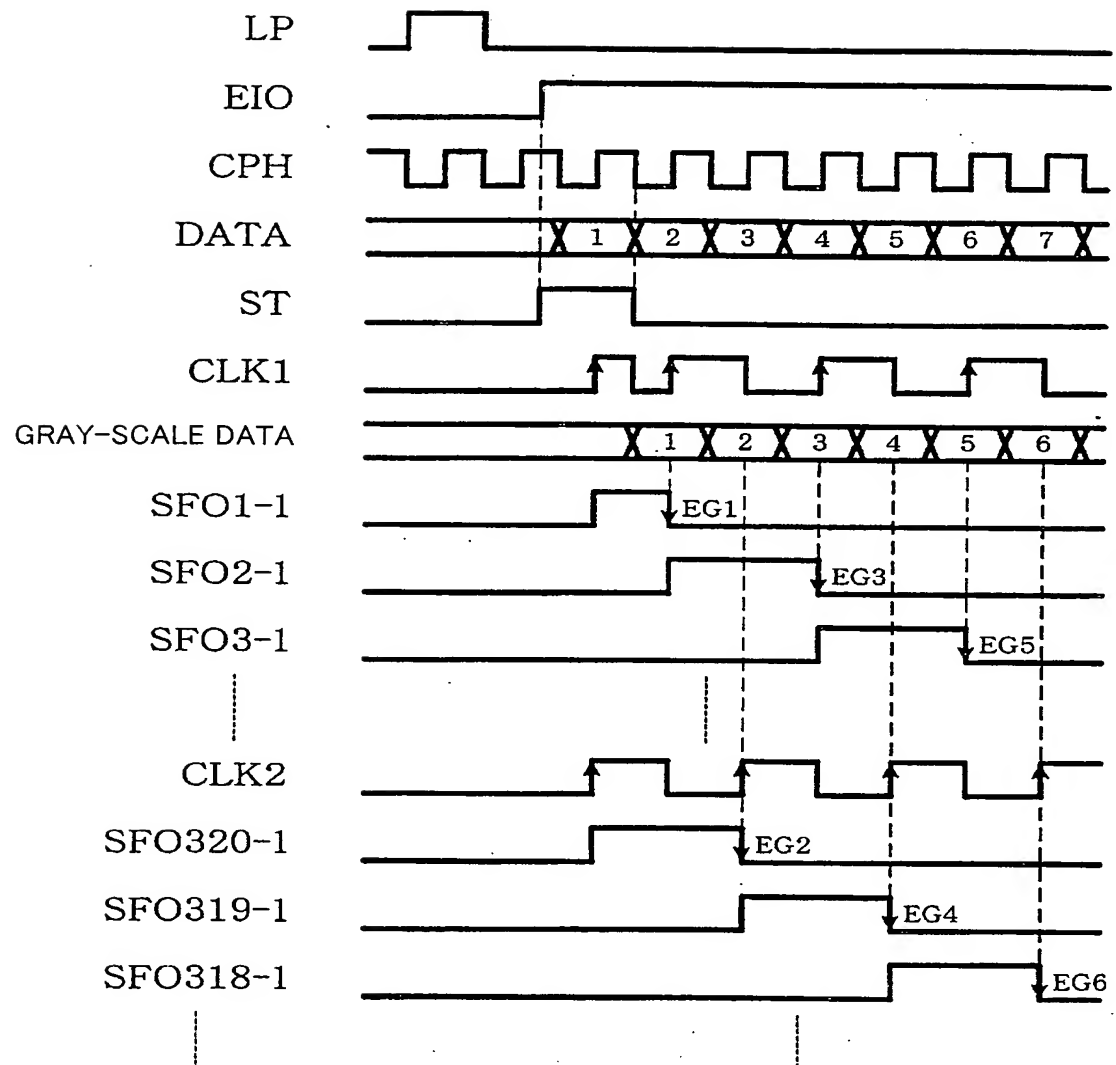


FIG. 28

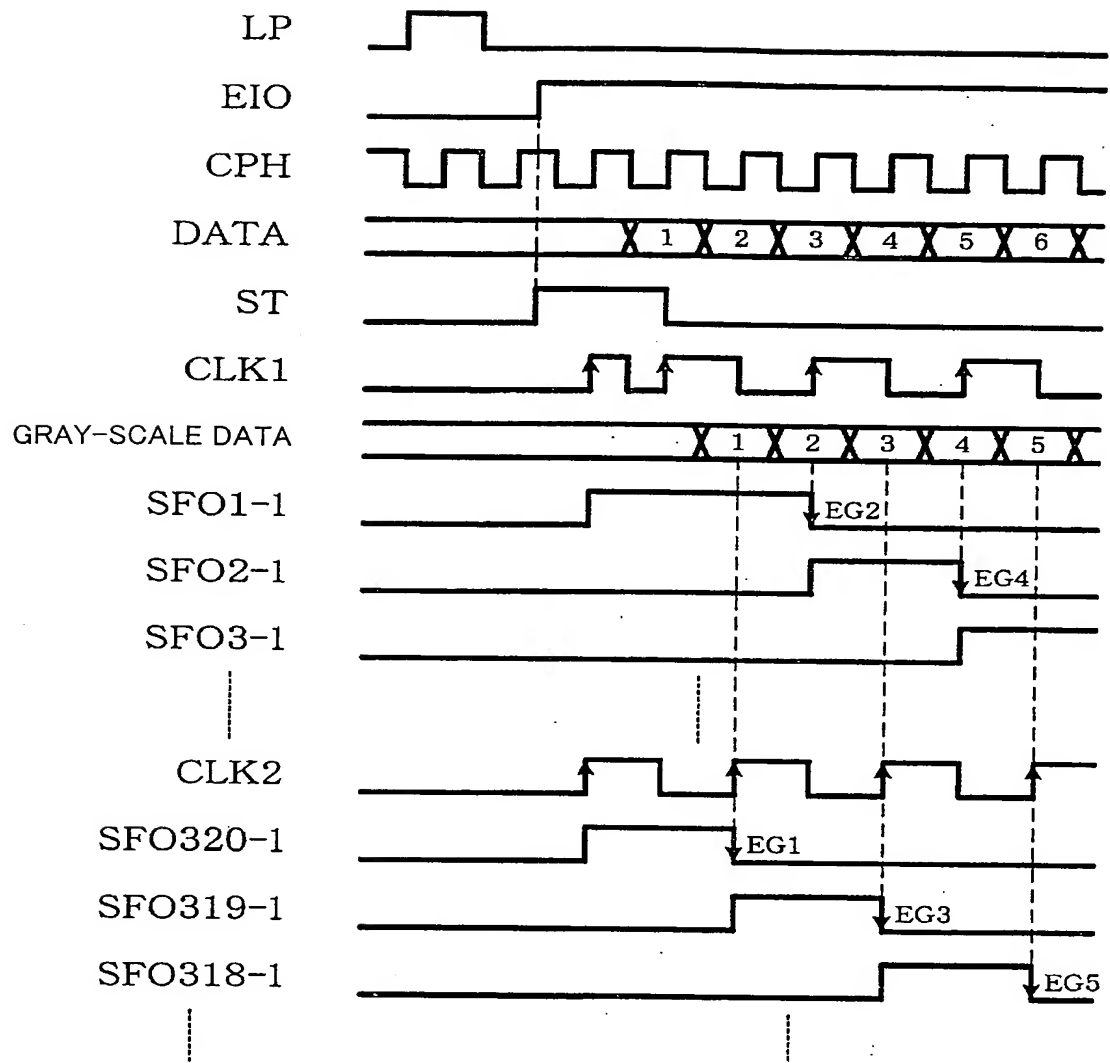


FIG. 29

MULTIPLEX	NUMBER OF CLOCK SIGNALS	FIRST GROUP		SECOND GROUP	
		FIRST CLOCK SIGNAL LINE	SECOND CLOCK SIGNAL LINE	FIRST CLOCK SIGNAL LINE	SECOND CLOCK SIGNAL LINE
3	EVEN NUMBER	CLK1	CLK2	—	—
	ODD NUMBER	CLK2	CLK1	—	—
6	$4 \times n$	CLK1	CLK3	CLK2	CLK4
	$4 \times n + 1$	CLK2	CLK4	CLK3	CLK1
	$4 \times n + 2$	CLK3	CLK1	CLK4	CLK2
	$4 \times n + 3$	CLK4	CLK2	CLK1	CLK3

FIG. 30

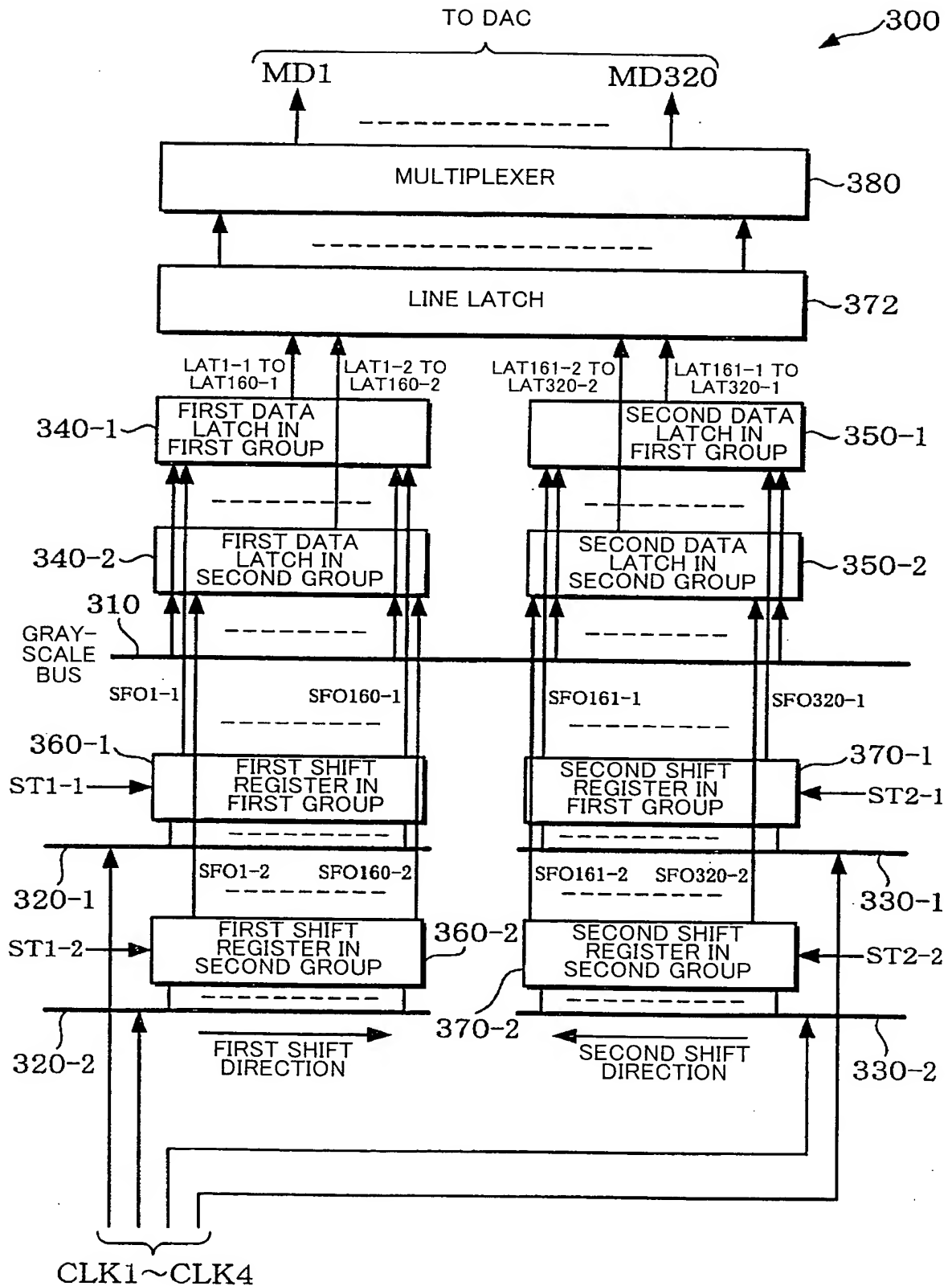


FIG. 31

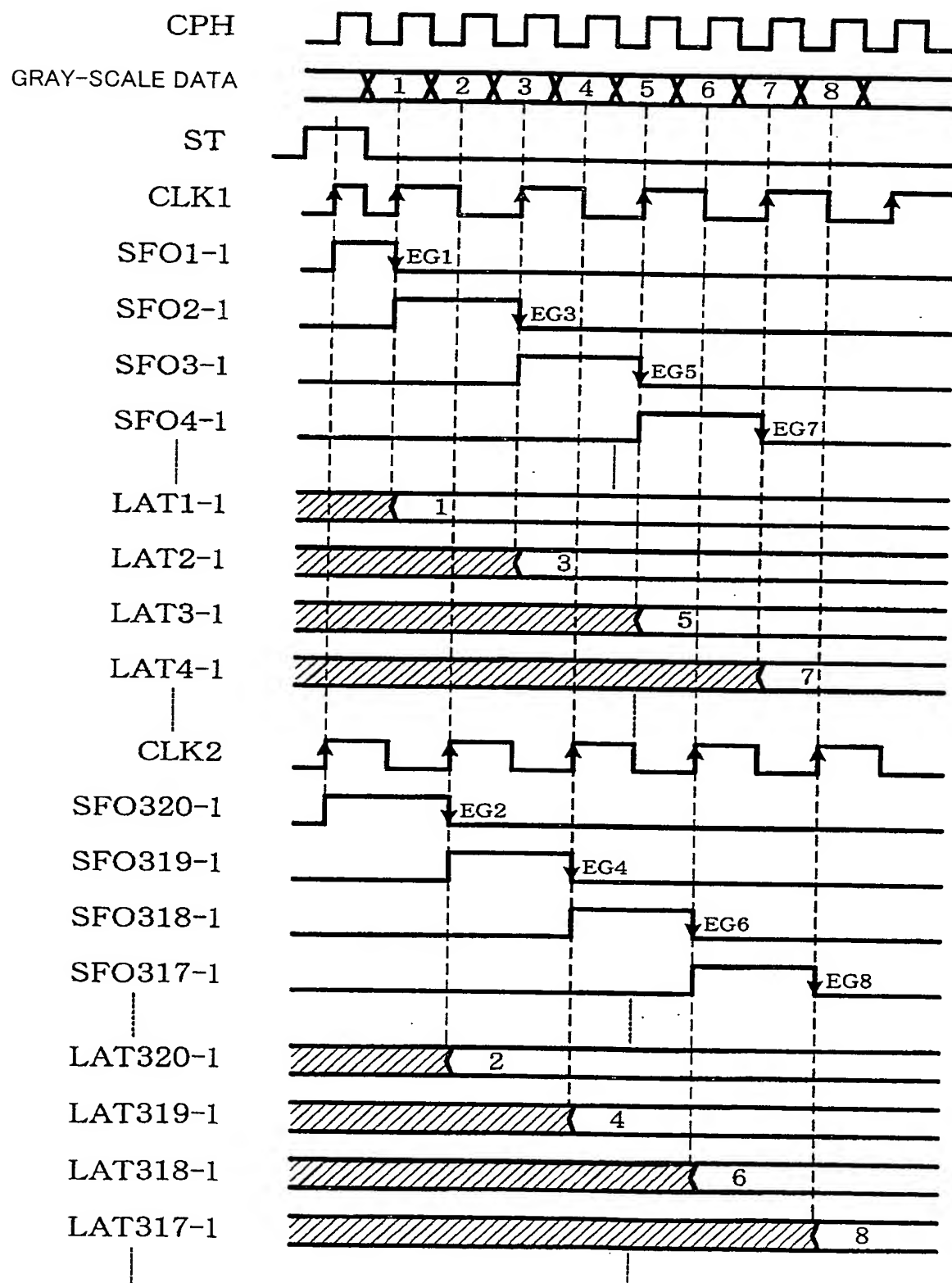


FIG. 32

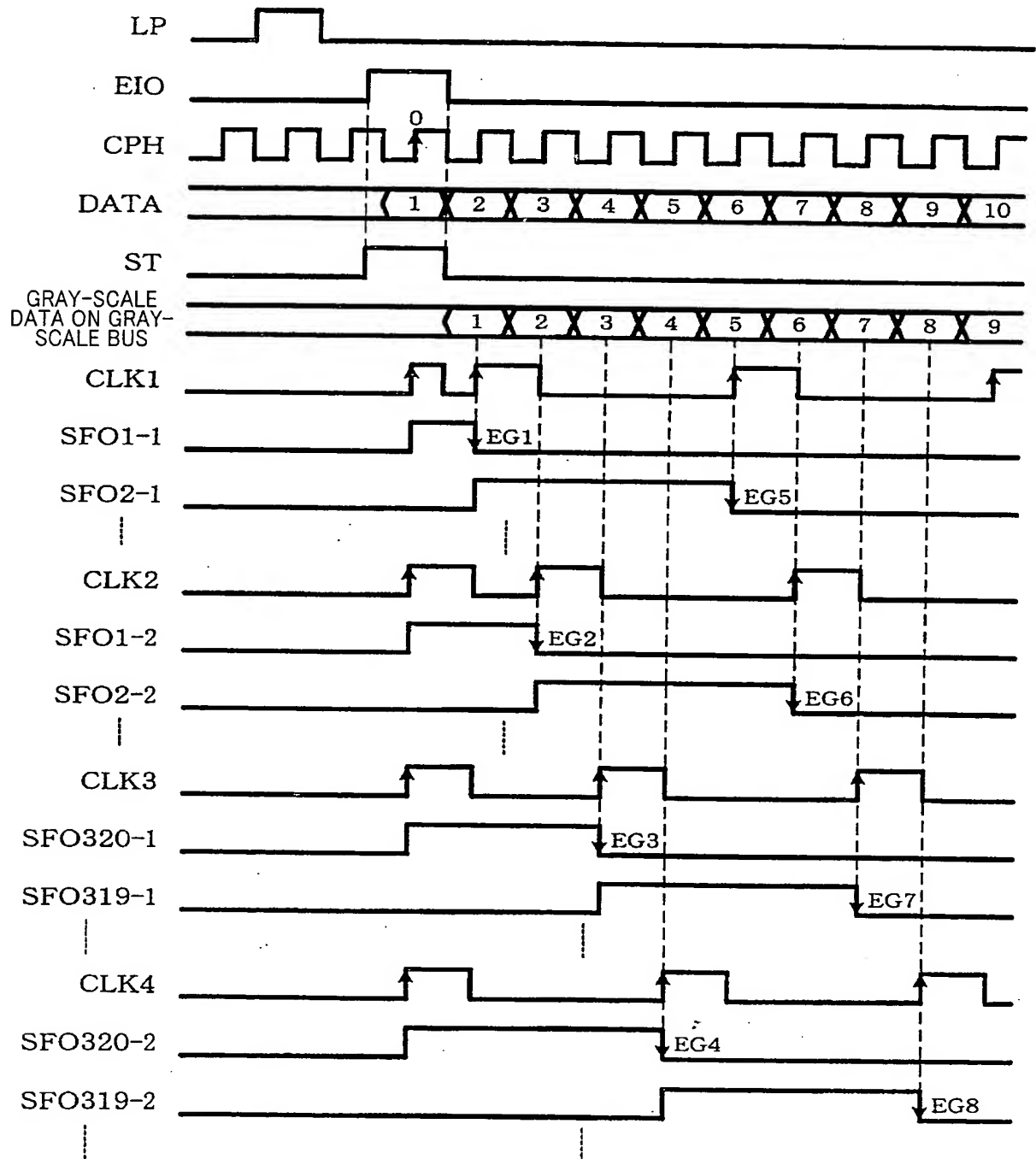


FIG. 33

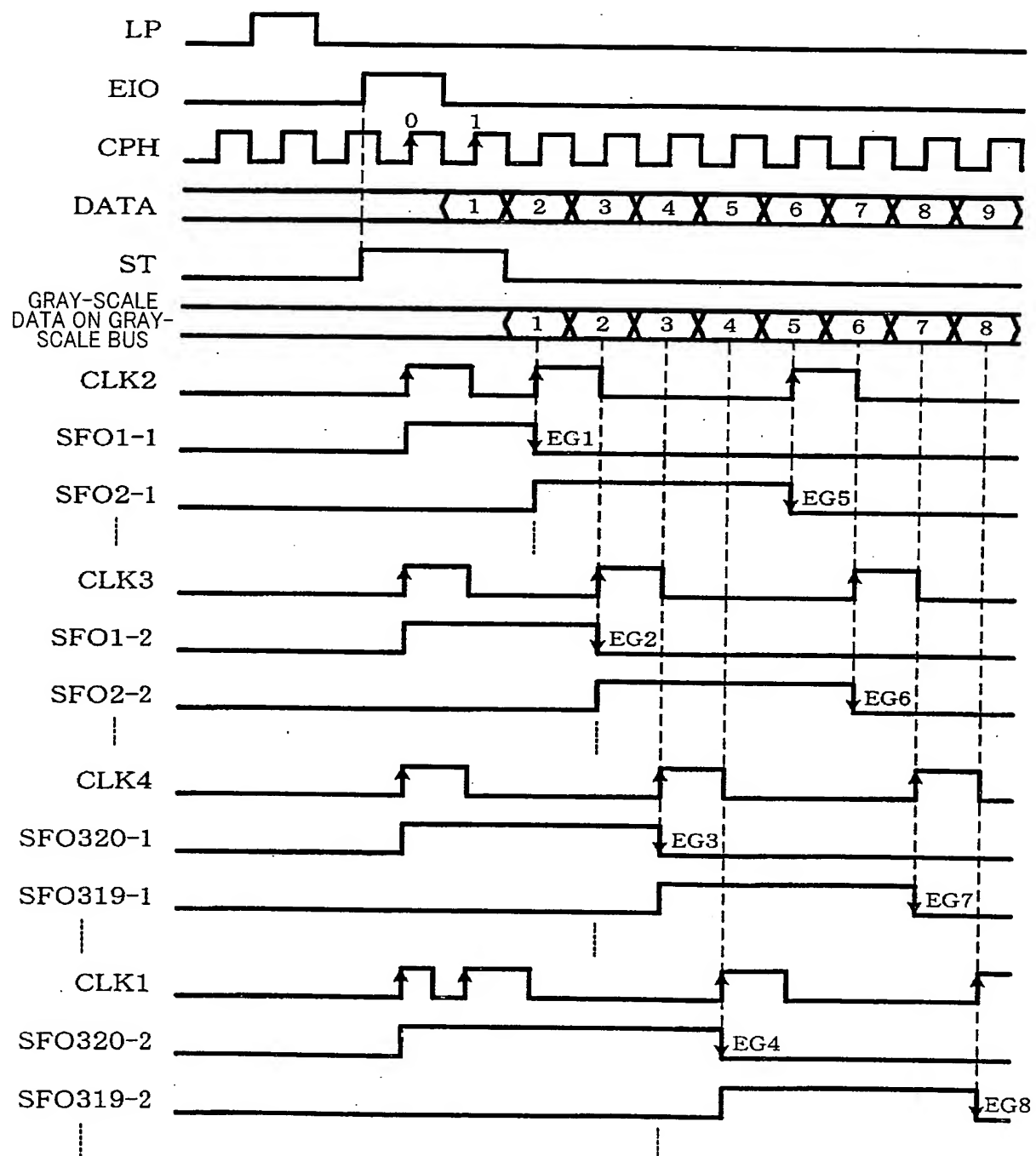


FIG. 34

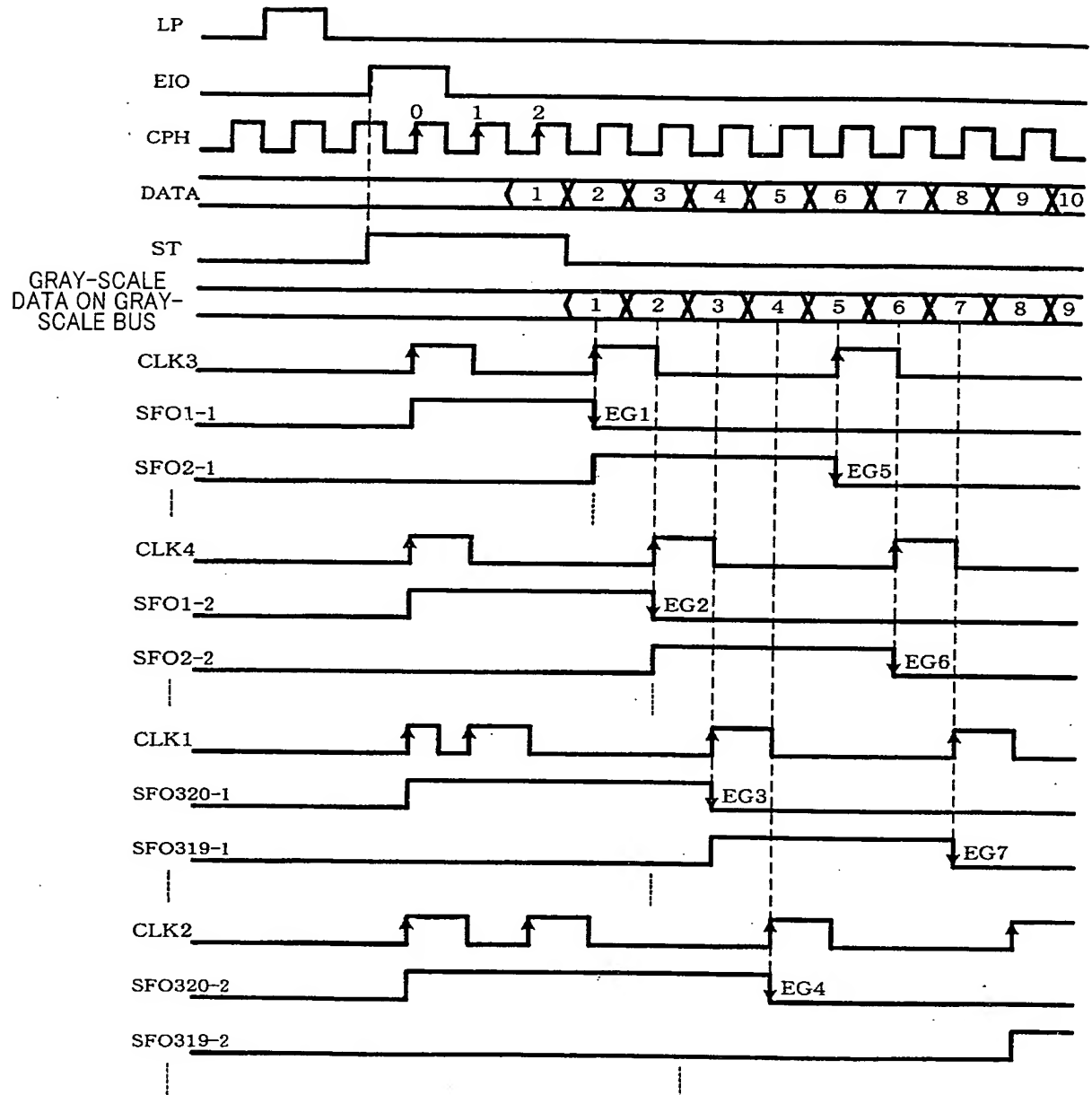


FIG. 35

